

Fig. 1 is a schematic diagram of a multi-layered structure 130. The structure 130 includes a first layer 132, a second layer 134, a third layer 136, and a fourth layer 138. The first layer 132 is a substrate layer. The second layer 134 is a patterned layer. The third layer 136 is a patterned layer. The fourth layer 138 is a patterned layer. The structure 130 is shown in a cross-sectional view.

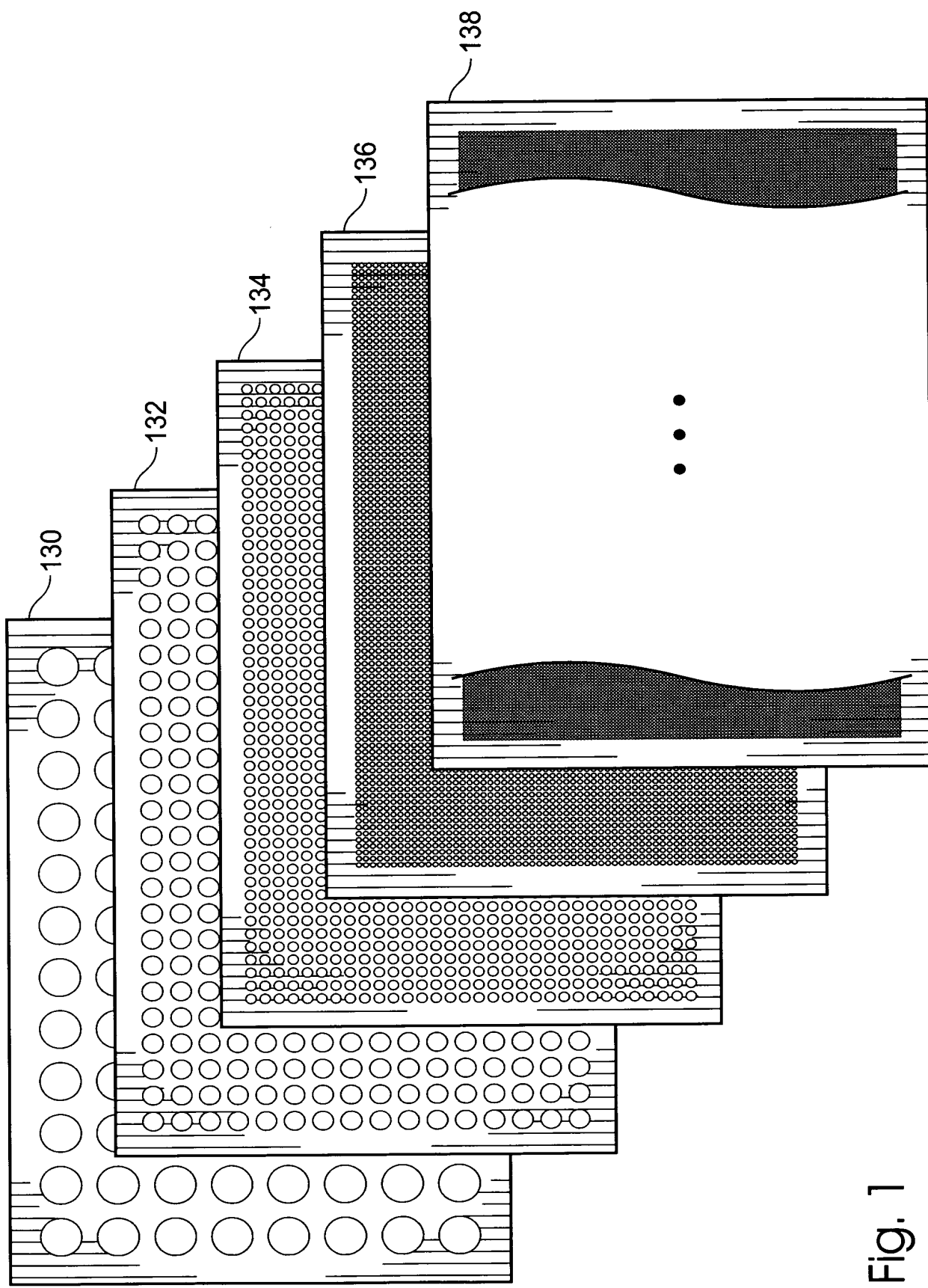


Fig. 1

Fig. 2

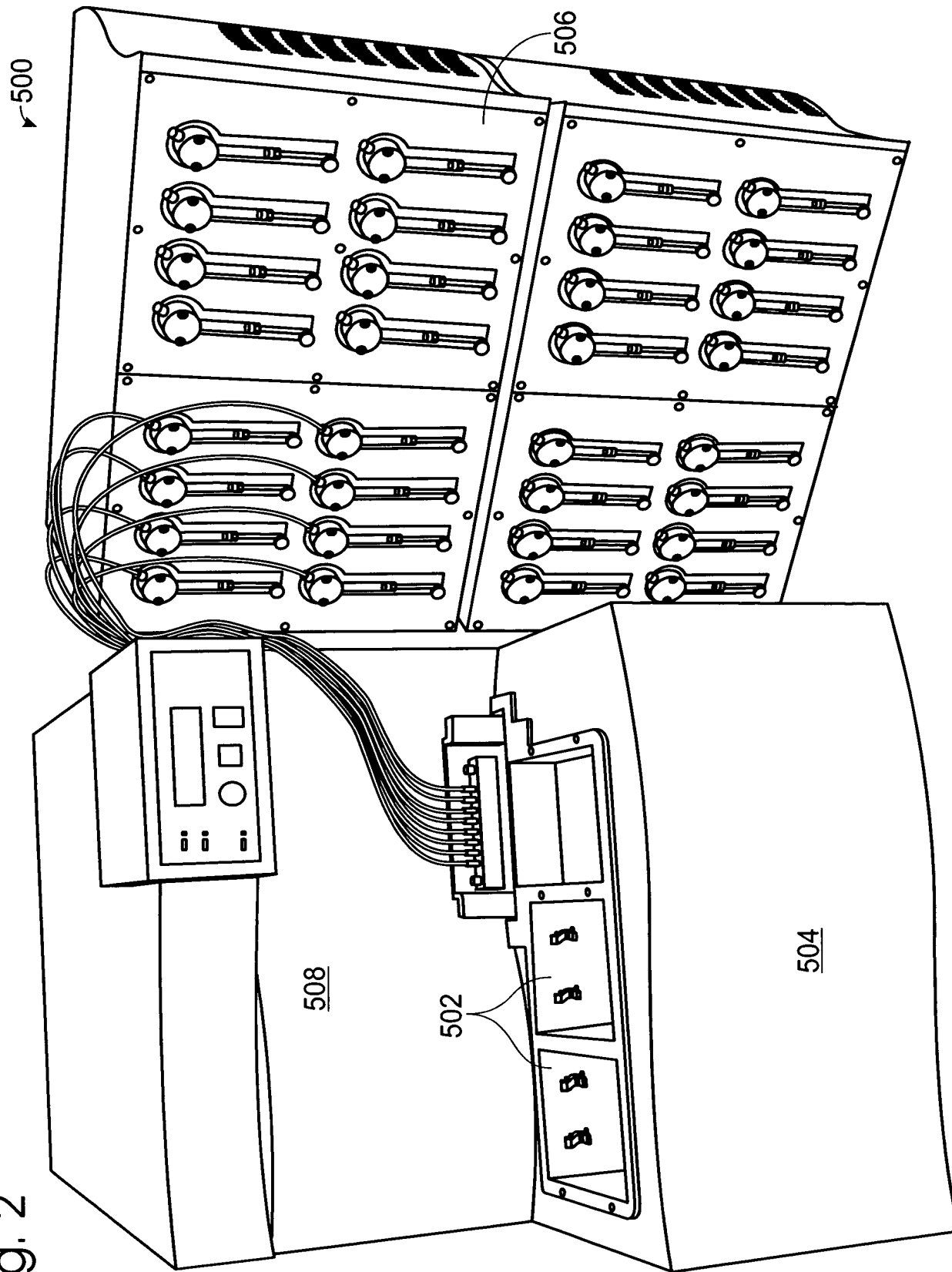


FIG. 2 is a perspective view of a control panel assembly 500. The control panel assembly 500 includes a control unit 508 and a main display area 504. The control unit 508 includes a display and buttons. The main display area 504 includes a multi-pin connector 502. The control unit 508 is connected to the multi-pin connector 502 by a bundle of cables 502. The control panel assembly 500 also includes a top edge 506.

Fig. 3

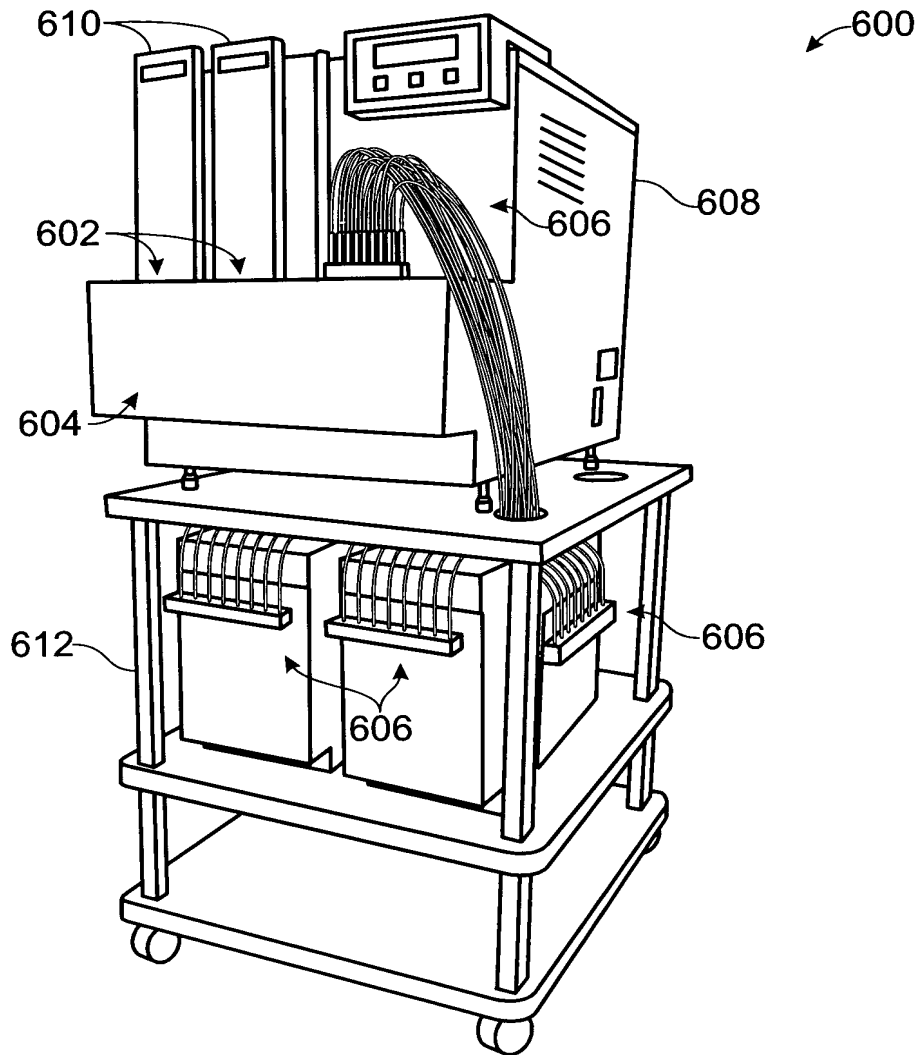
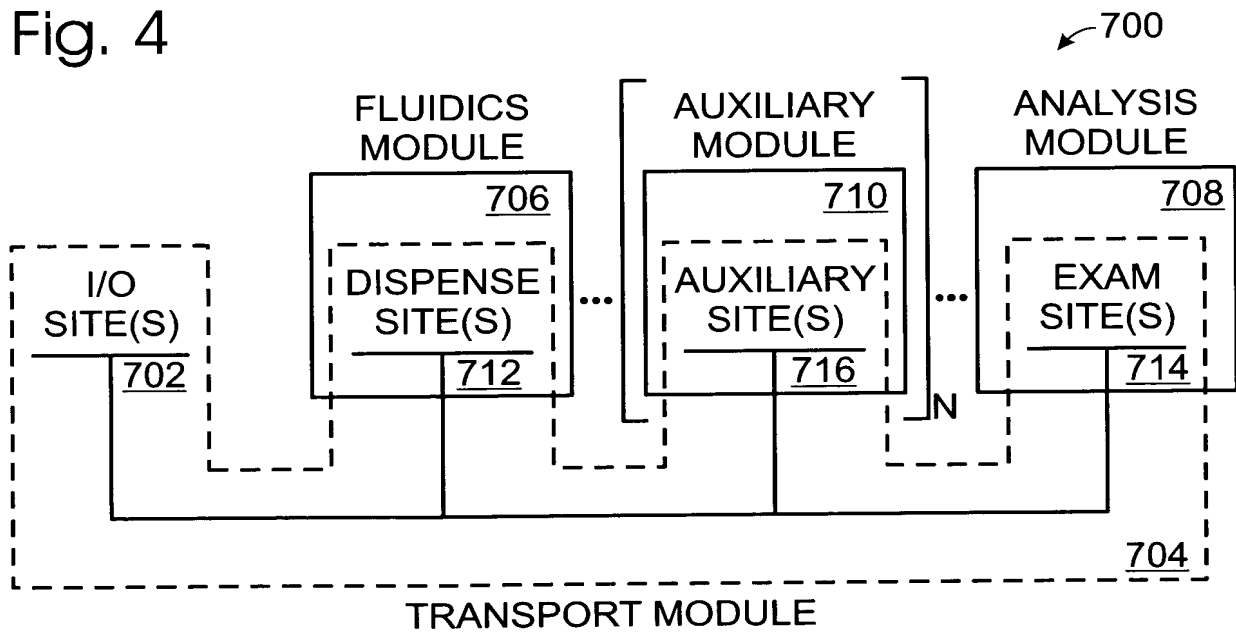


Fig. 4



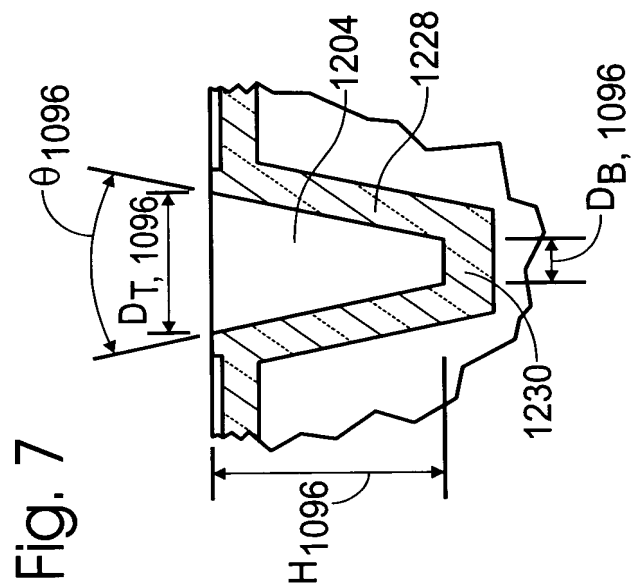
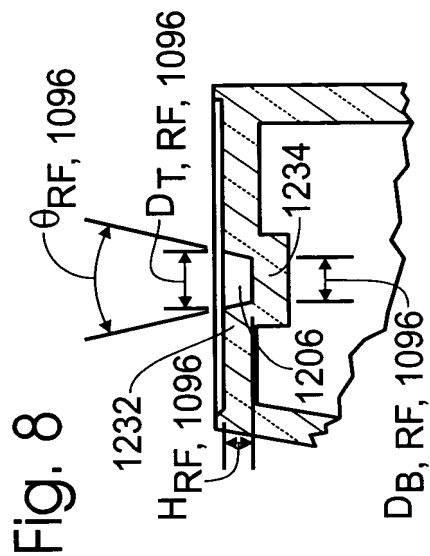
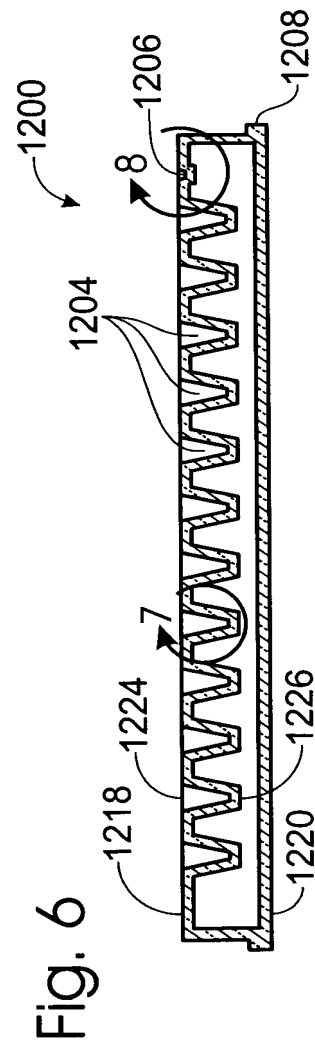
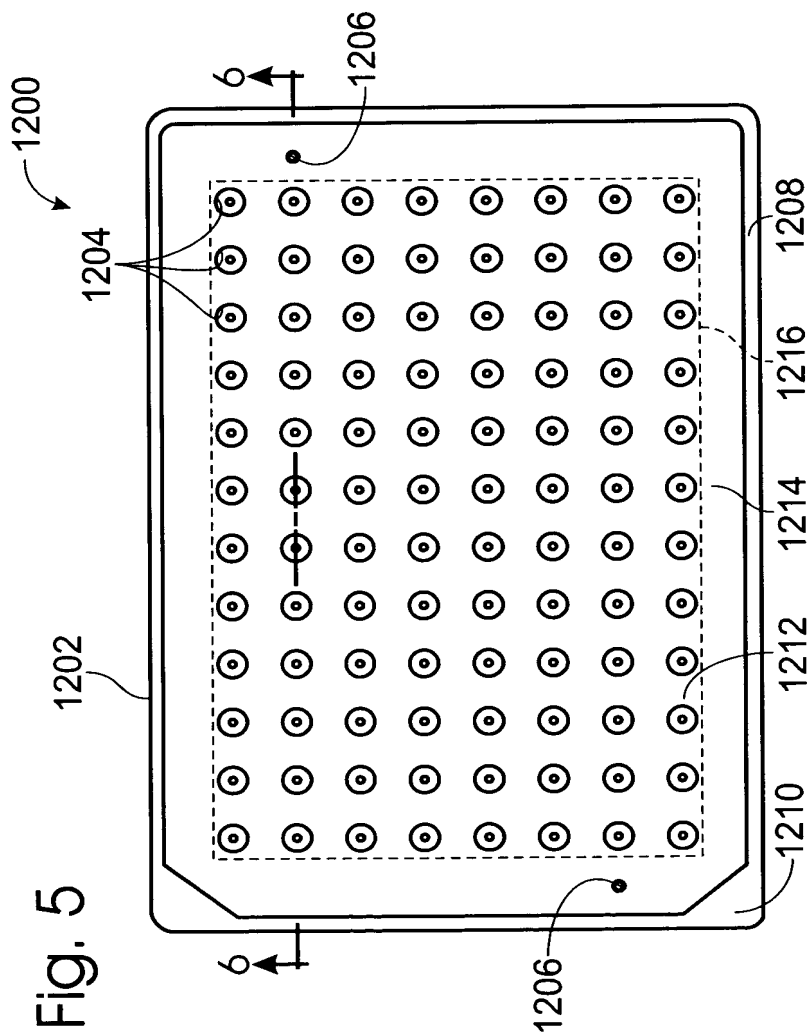




Fig. 13

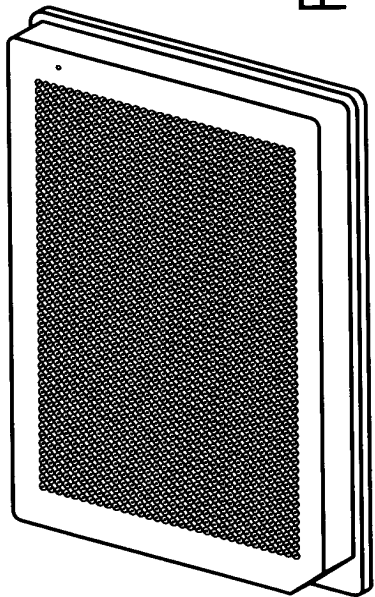


Fig. 15

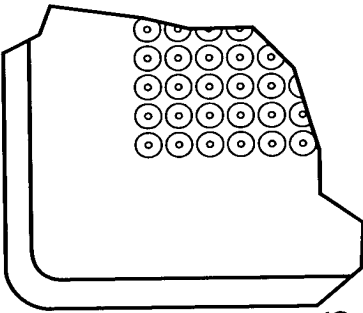


Fig. 14

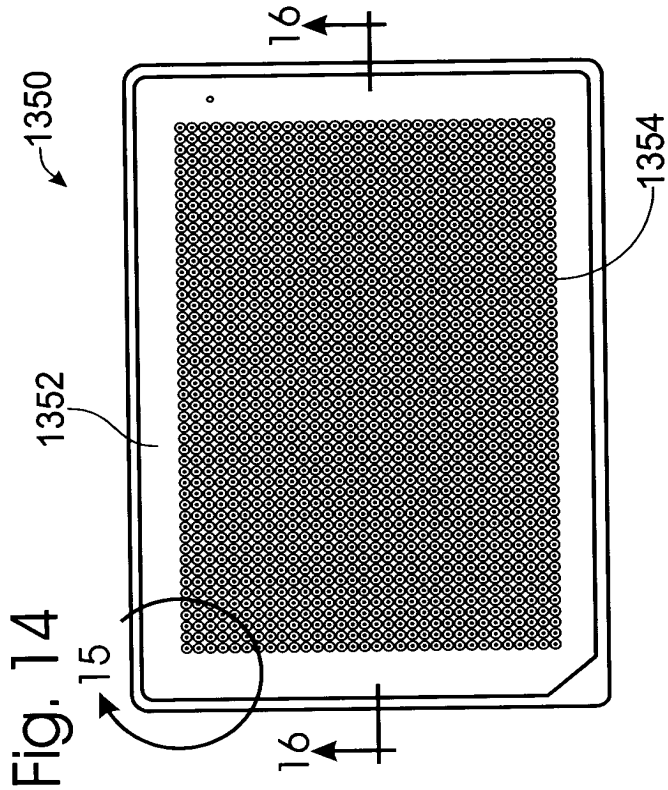


Fig. 17

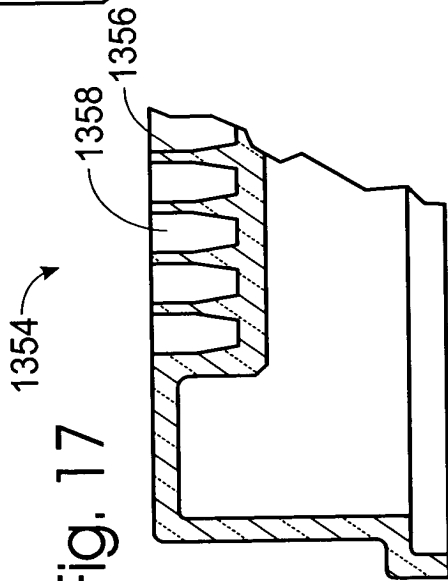
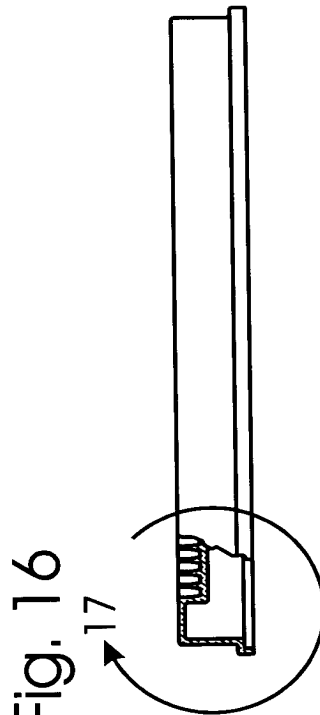


Fig. 16



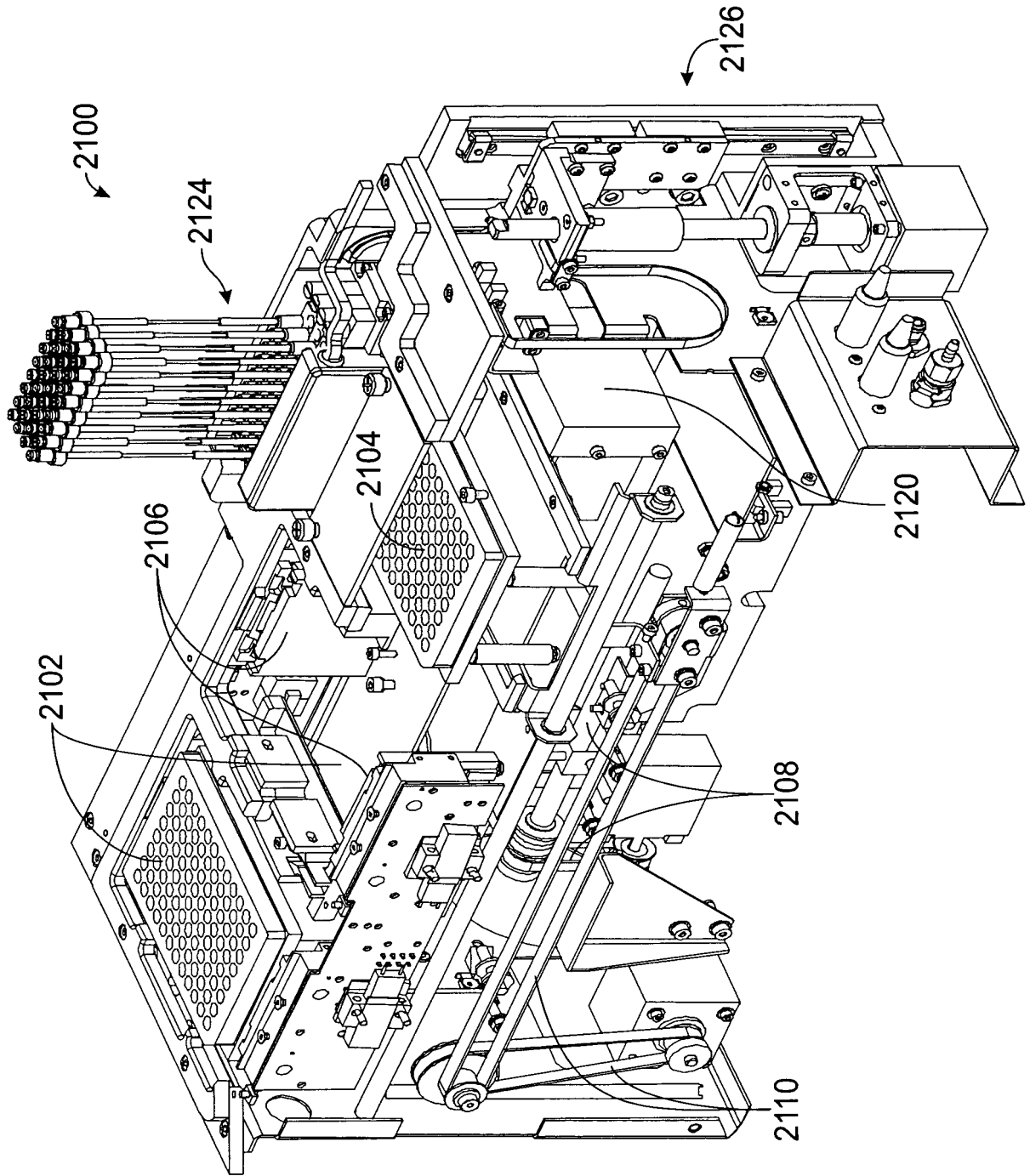


Fig. 18

Fig. 19

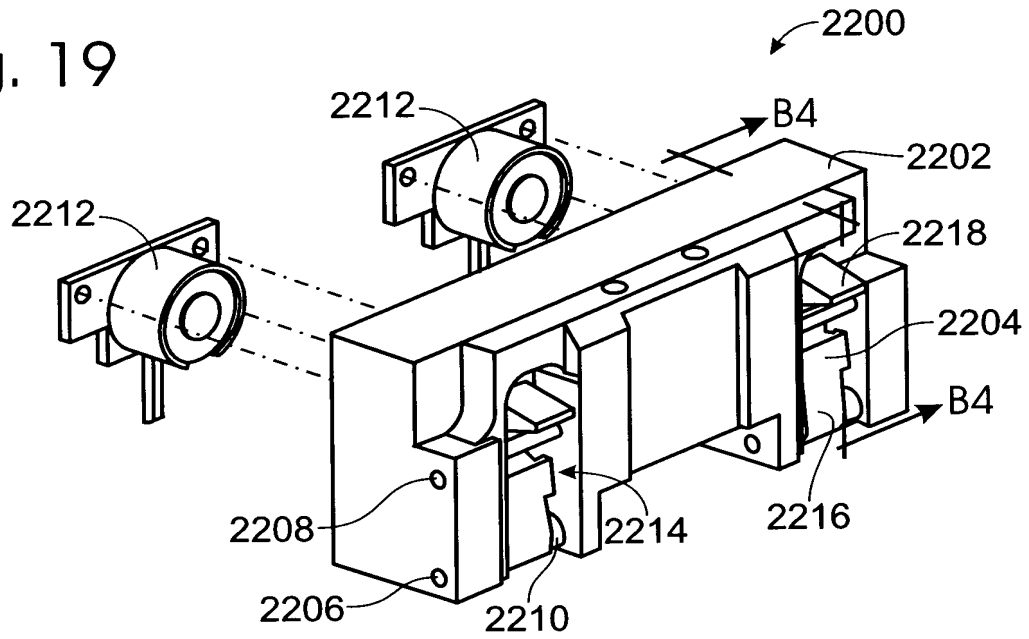


Fig. 20

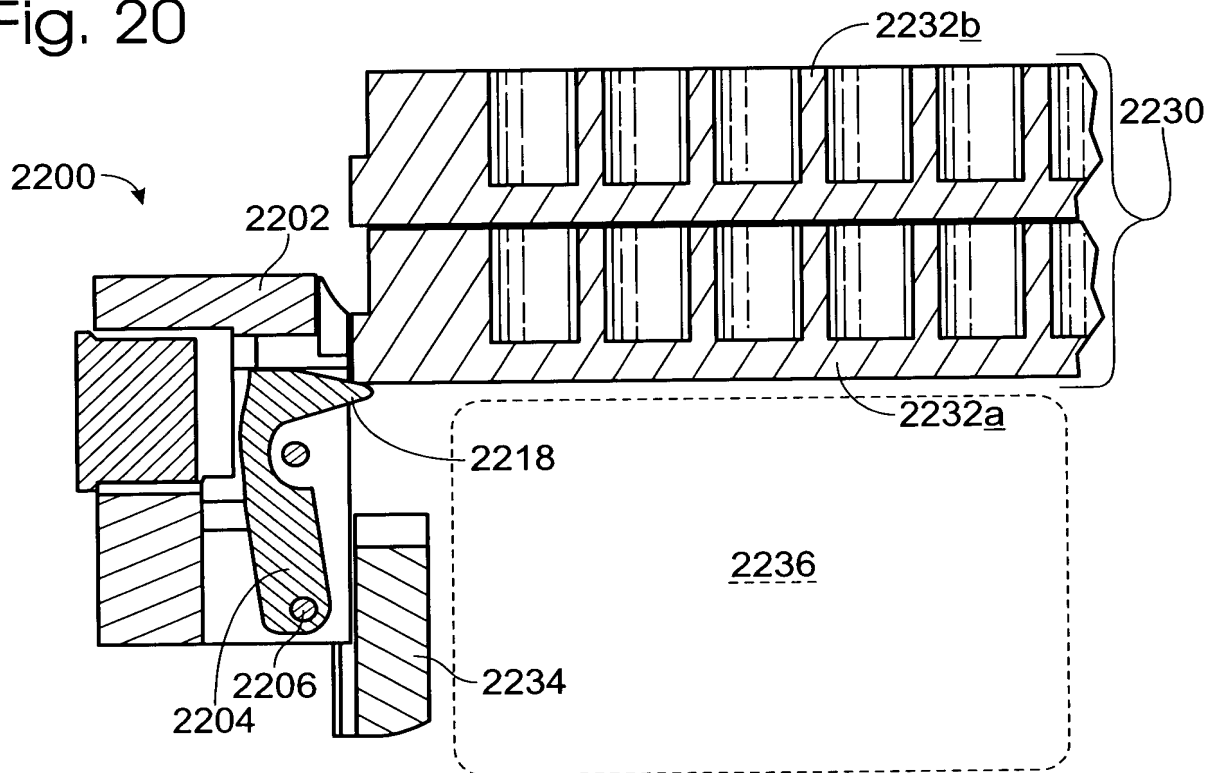




Fig. 21 (INPUT CYCLE)

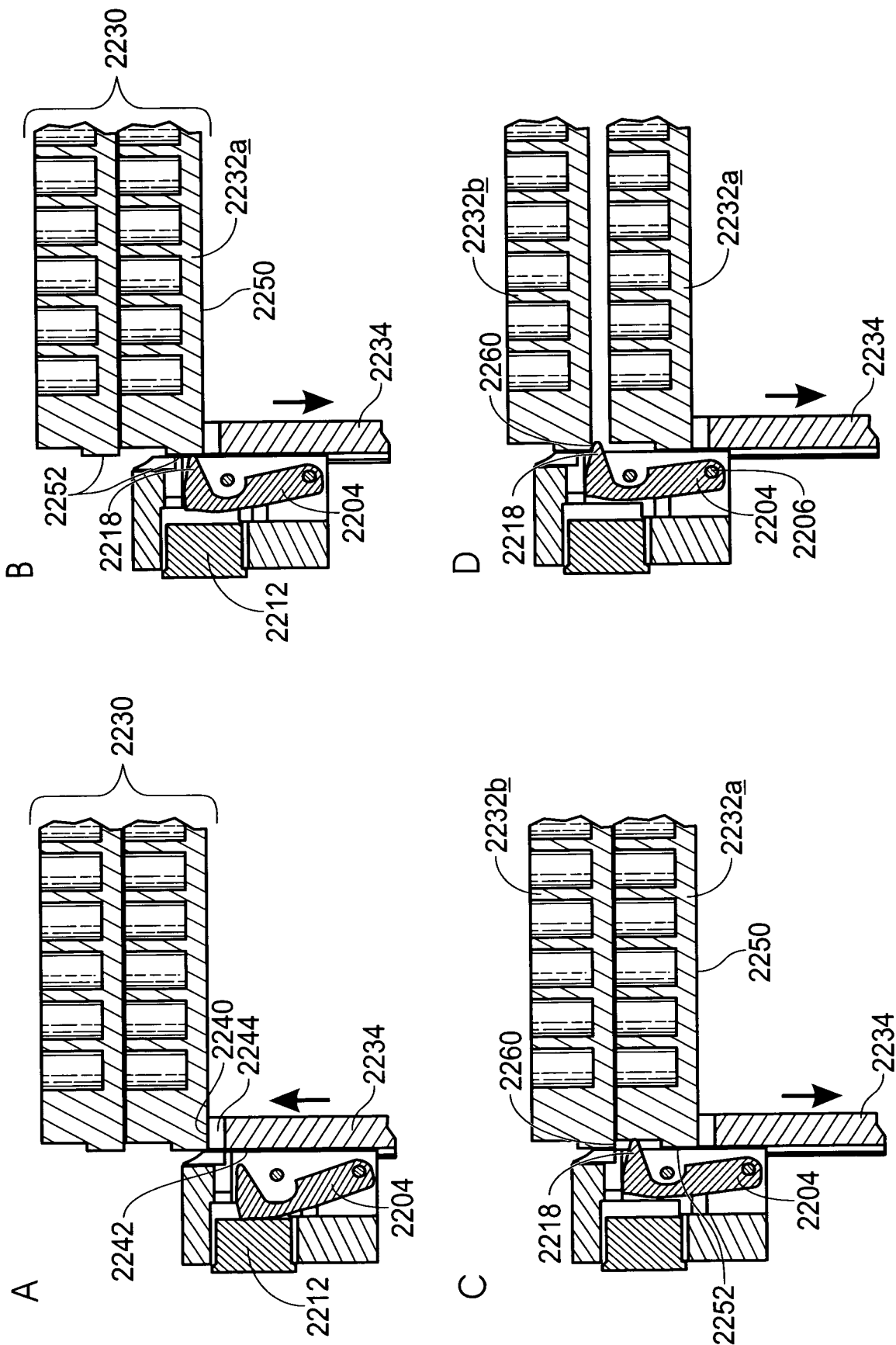


Fig. 22 (OUTPUT CYCLE)

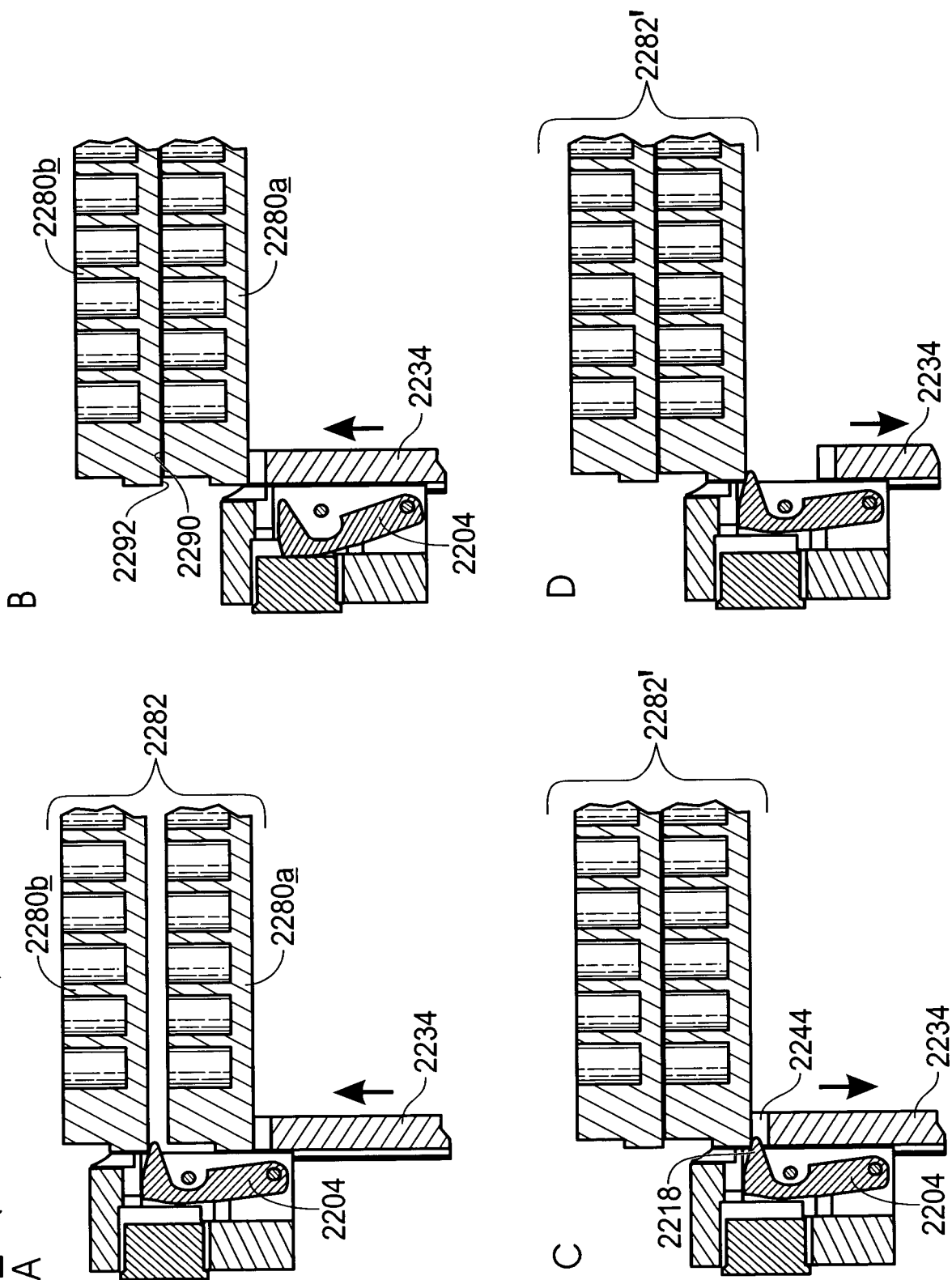


Fig. 23

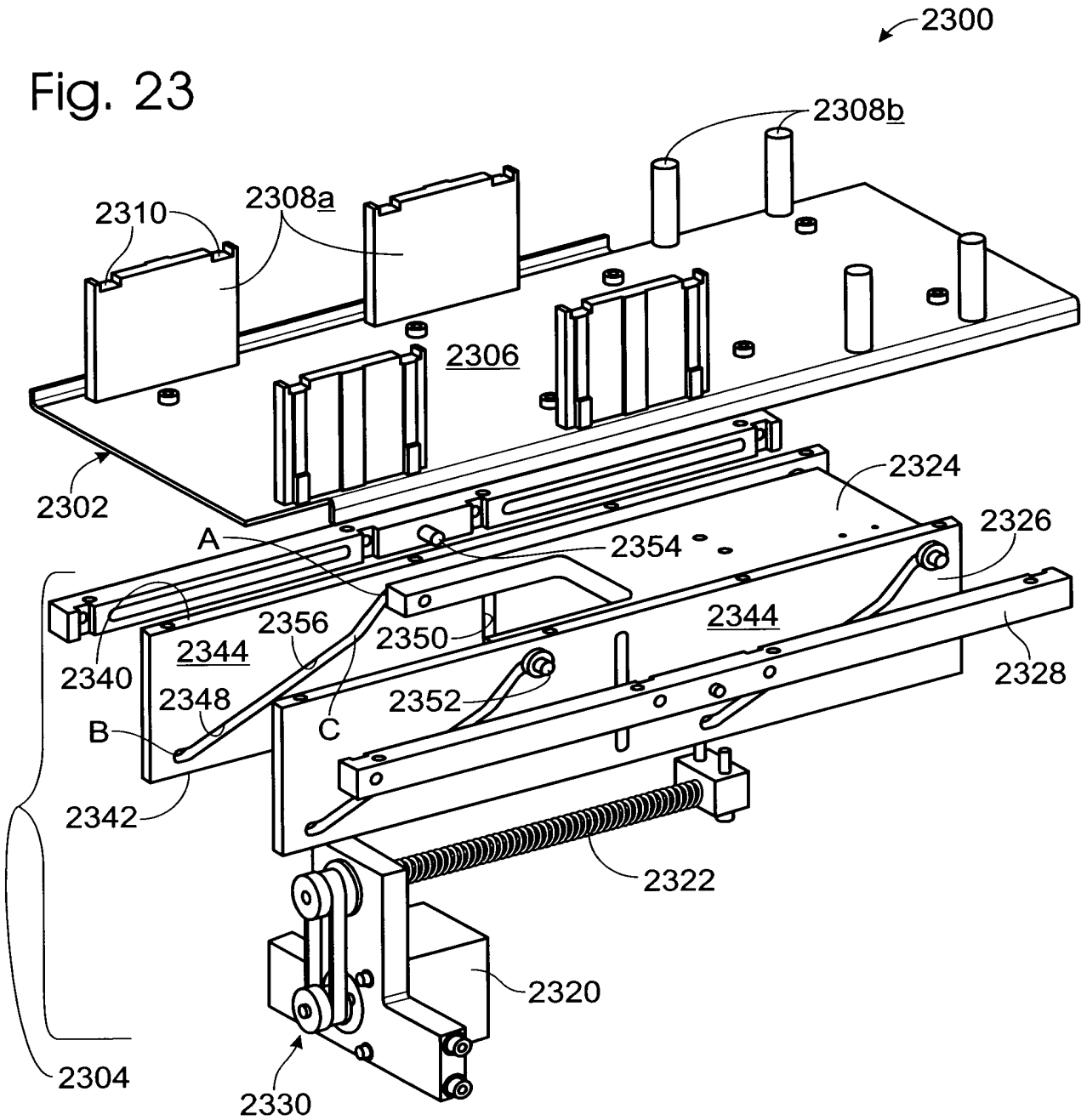




Fig. 25

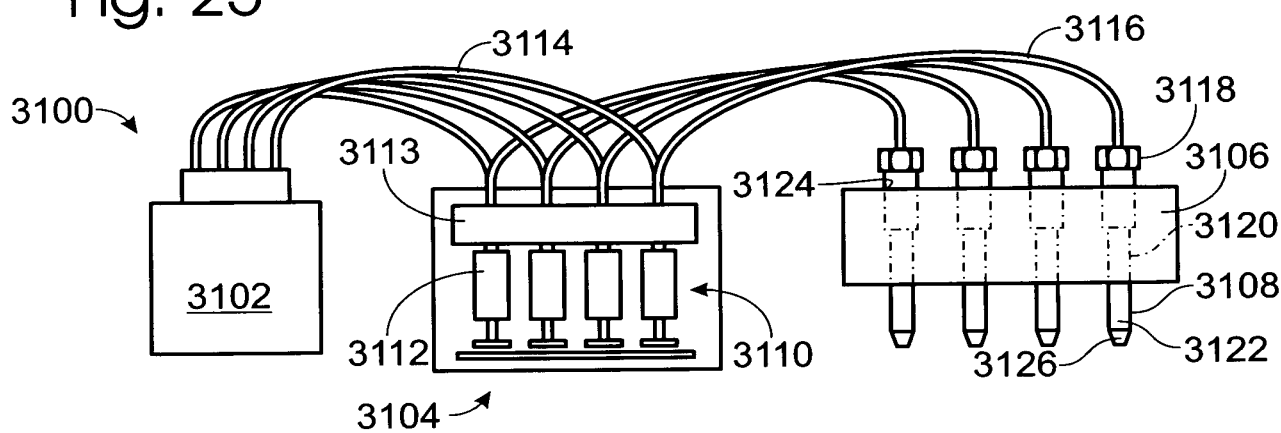


Fig. 26

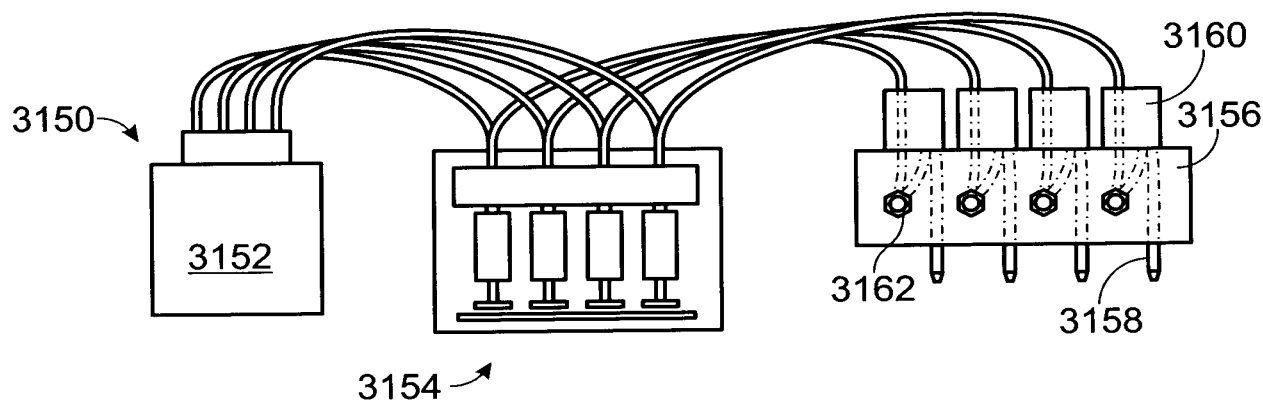


Fig. 27

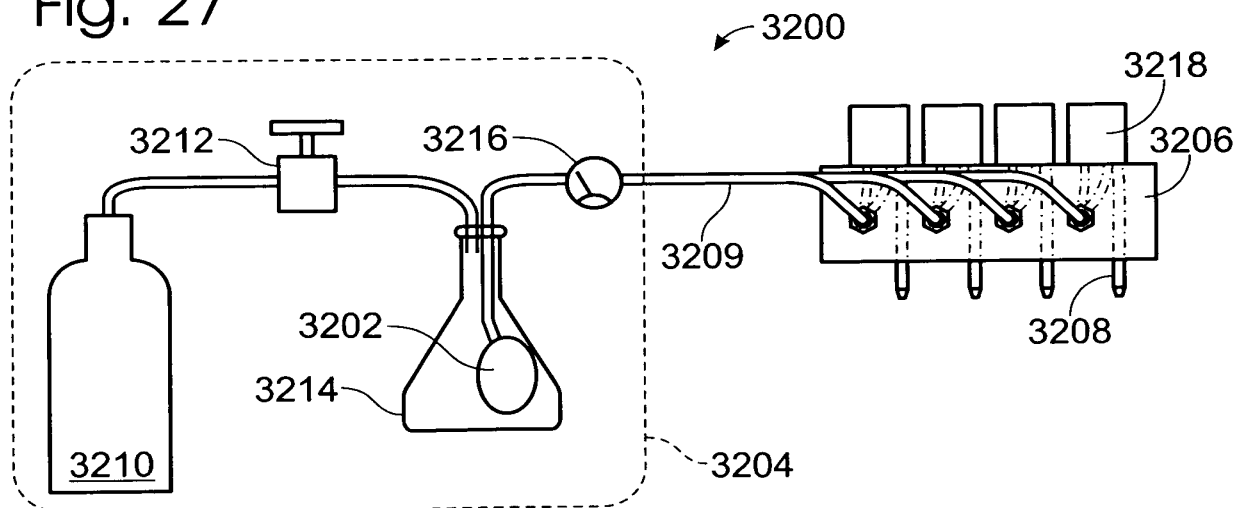


Fig. 28

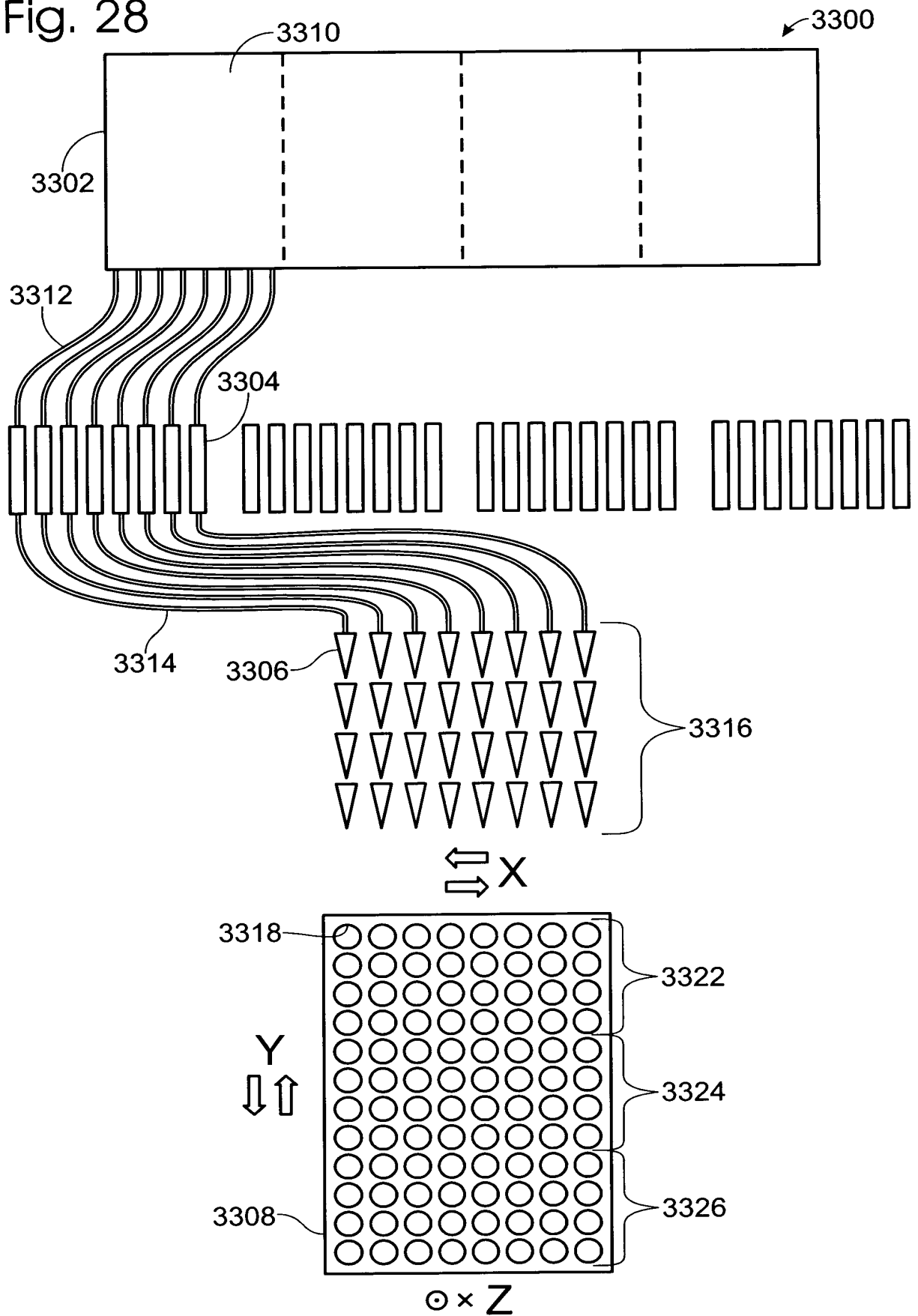


Fig. 29

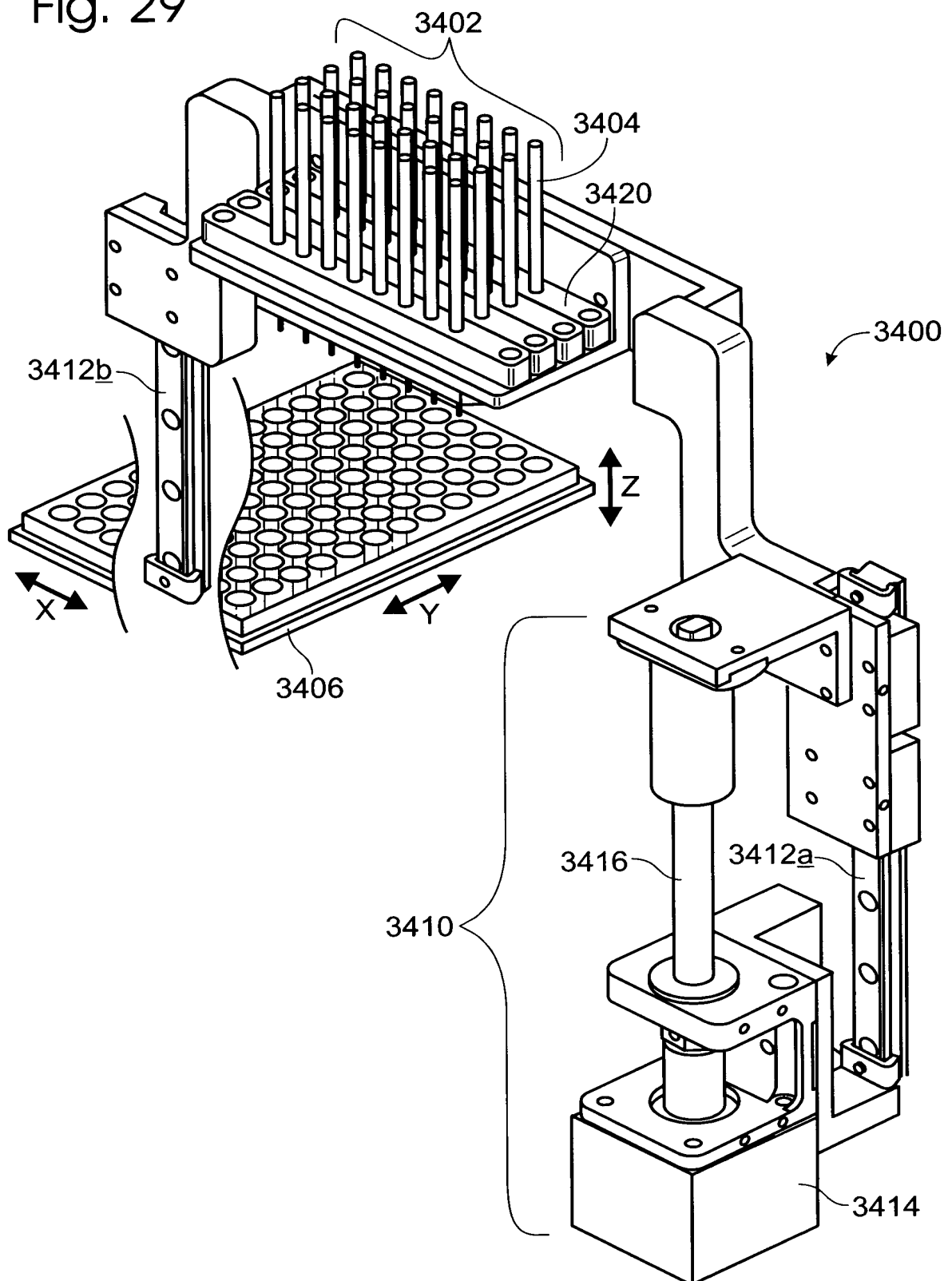


Fig. 30

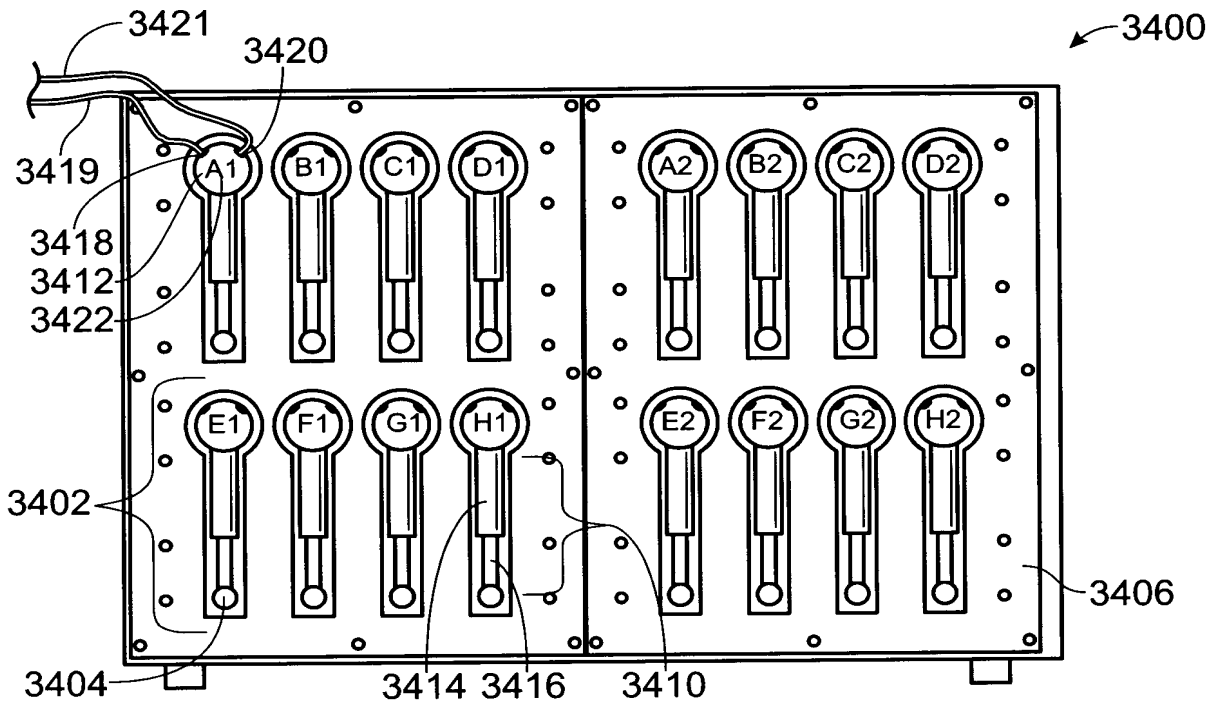


Fig. 31

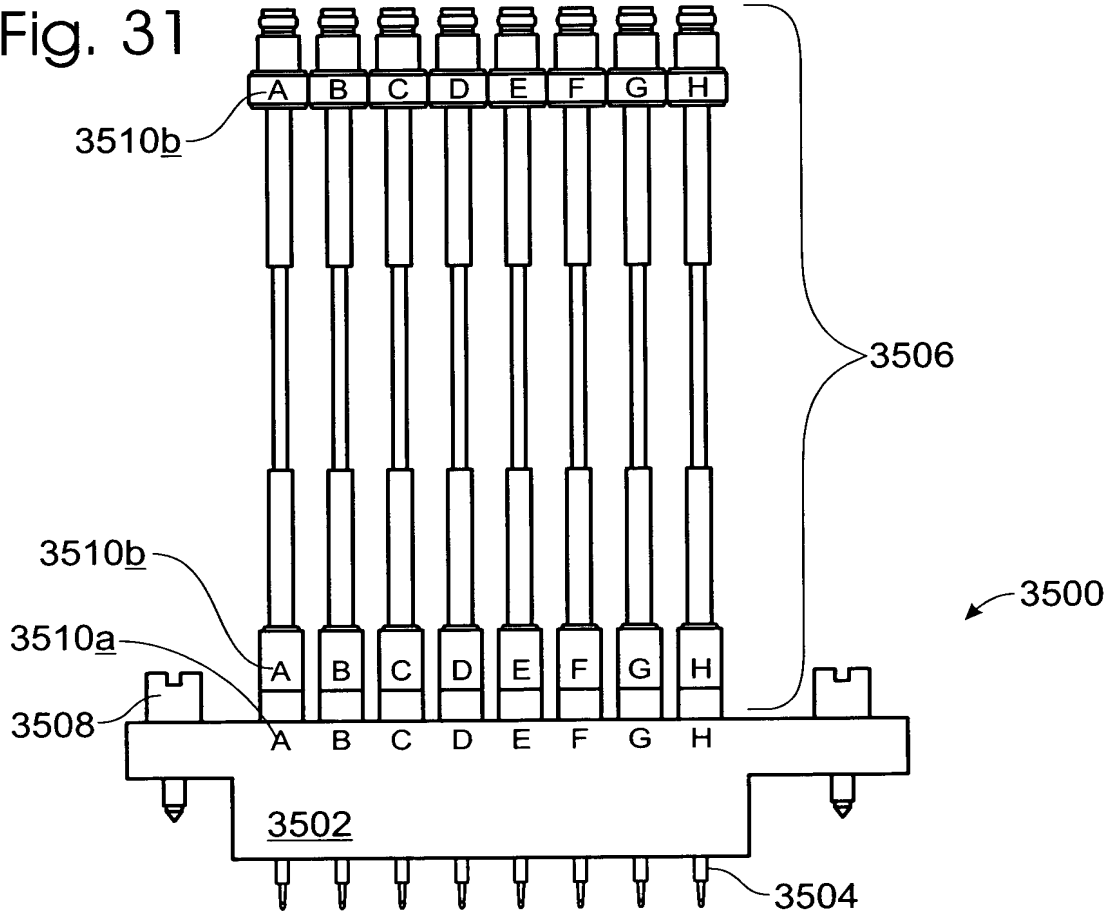




Fig. 32

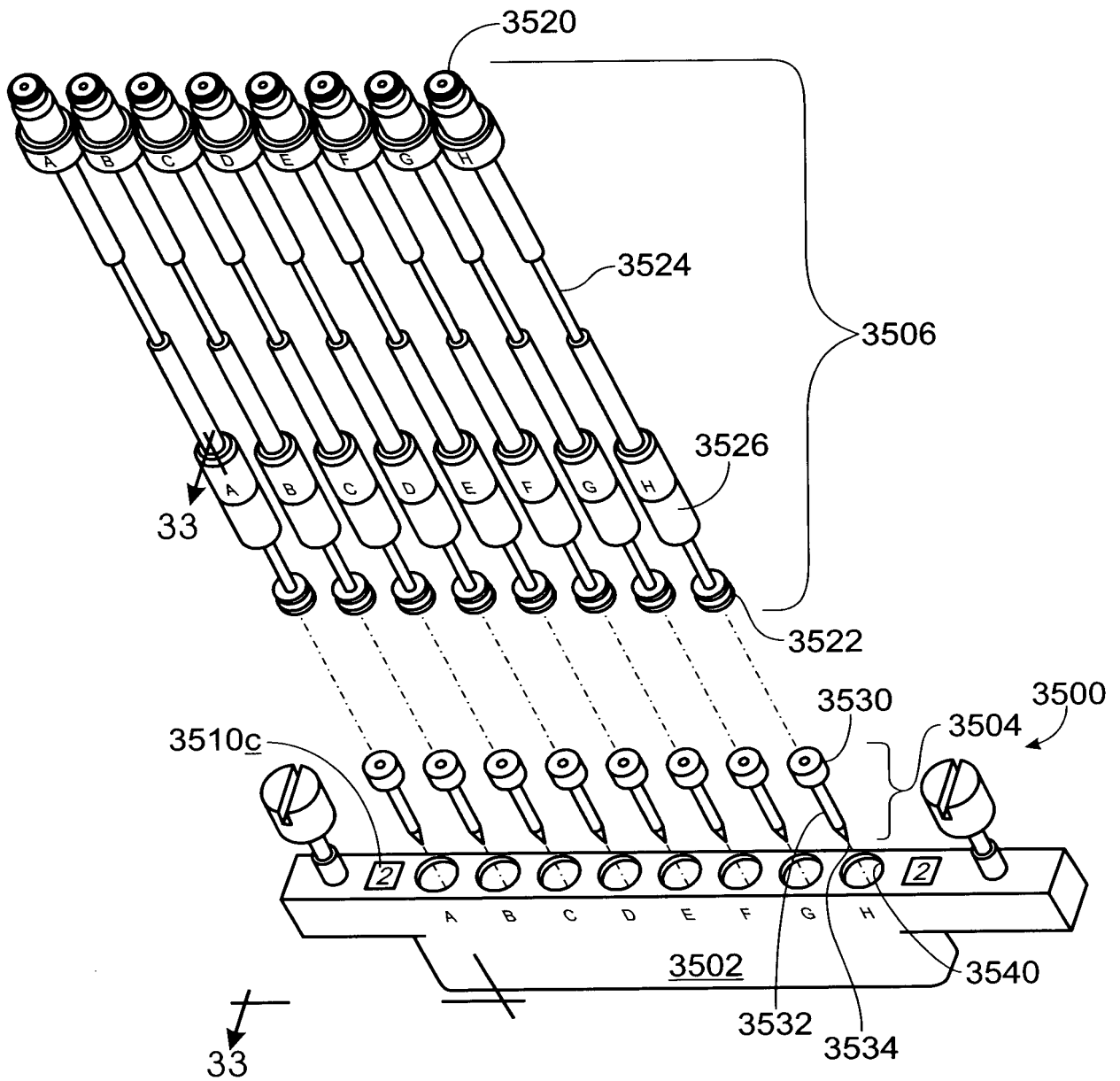


Fig. 33

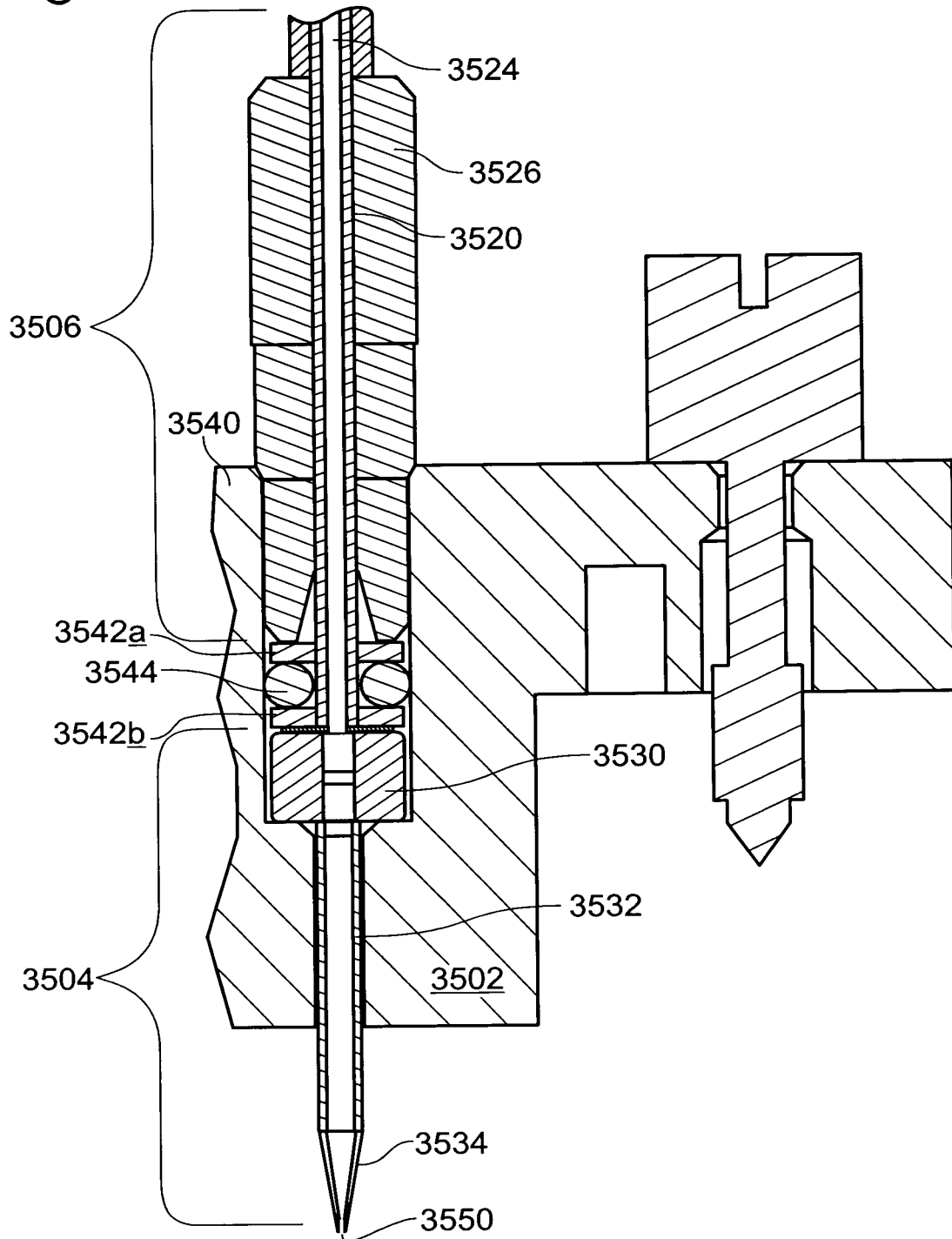


Fig. 34

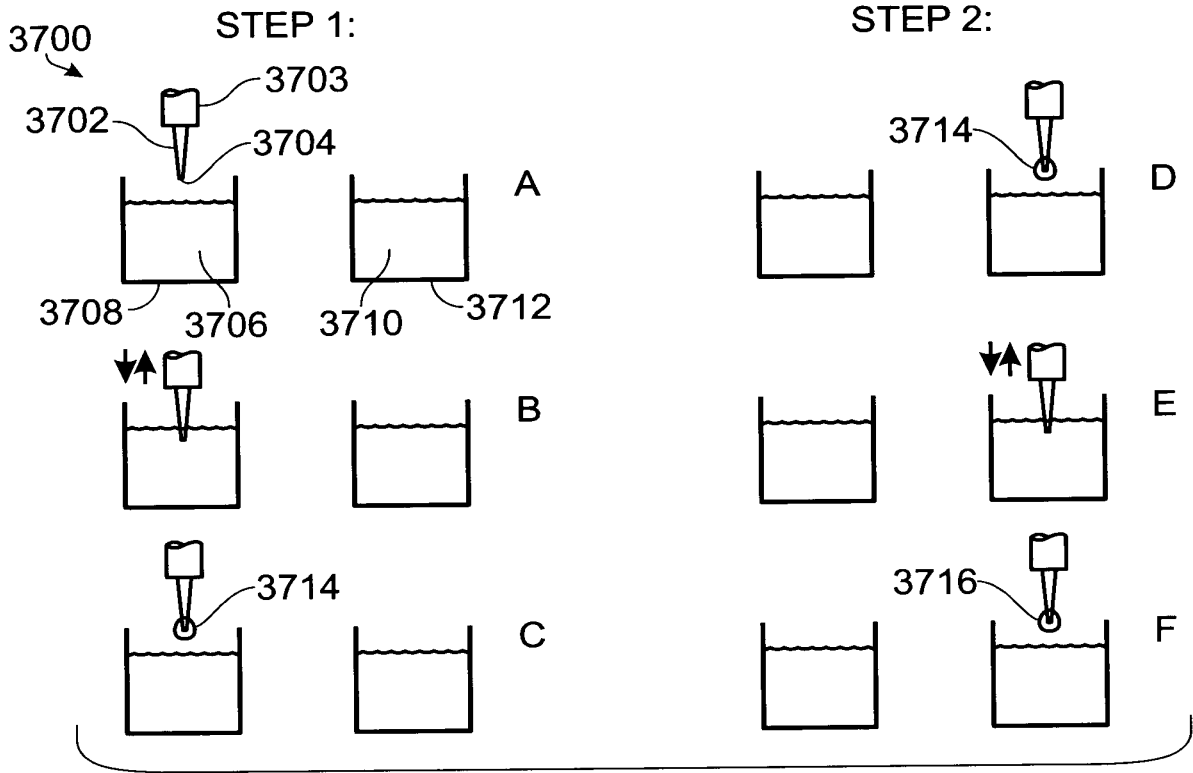


Fig. 35

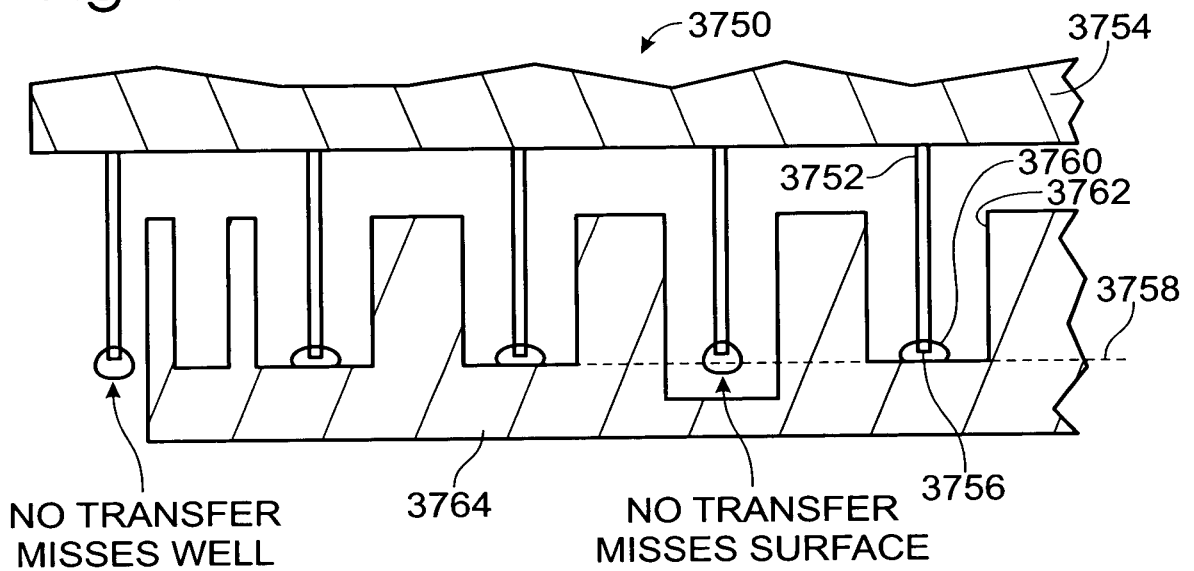


Fig. 36

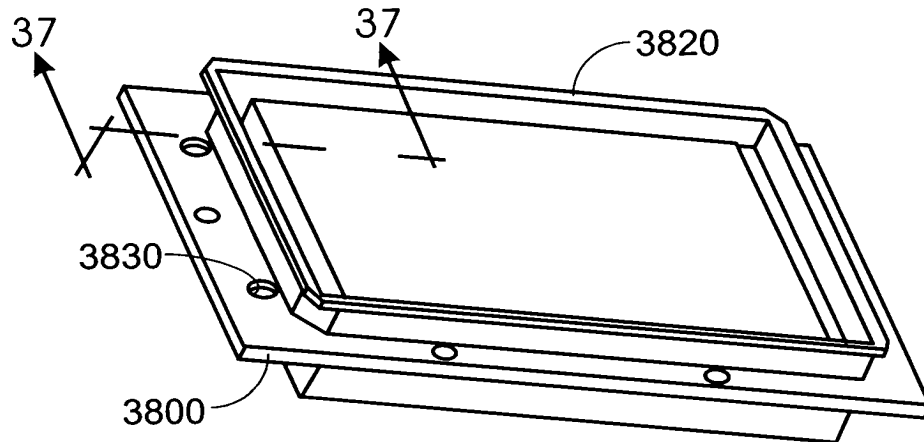


Fig. 37

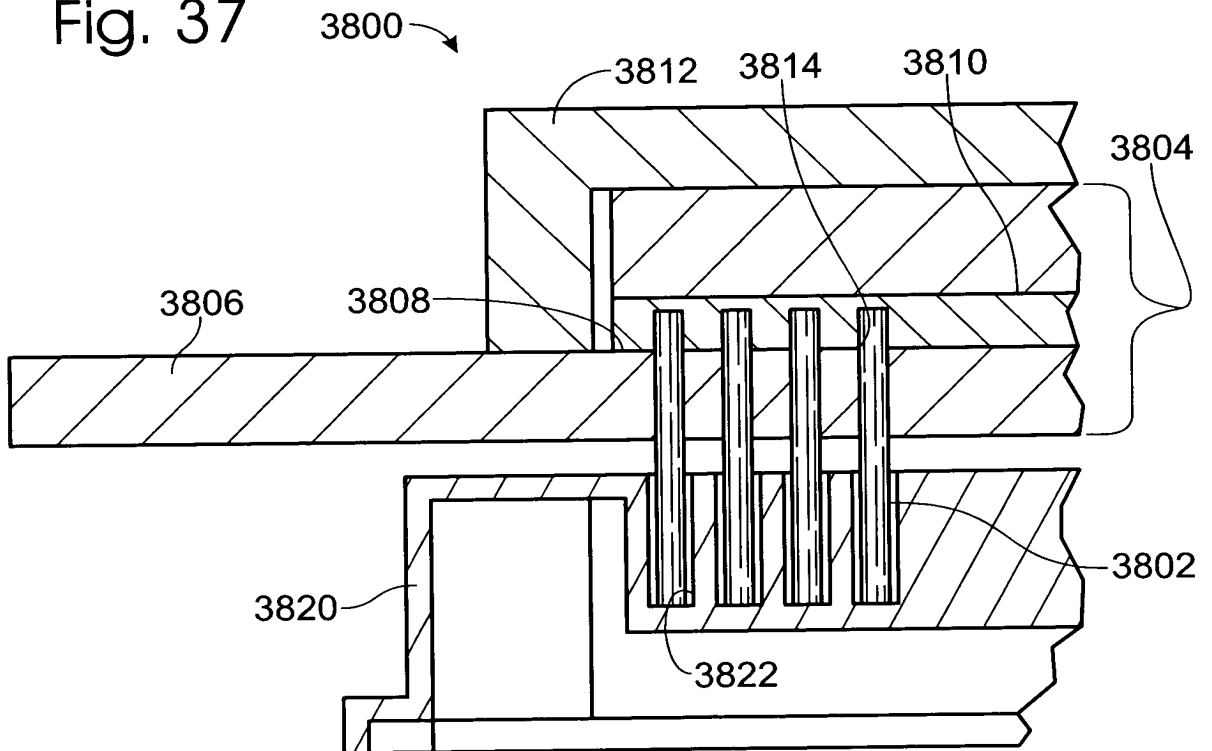


Fig. 38

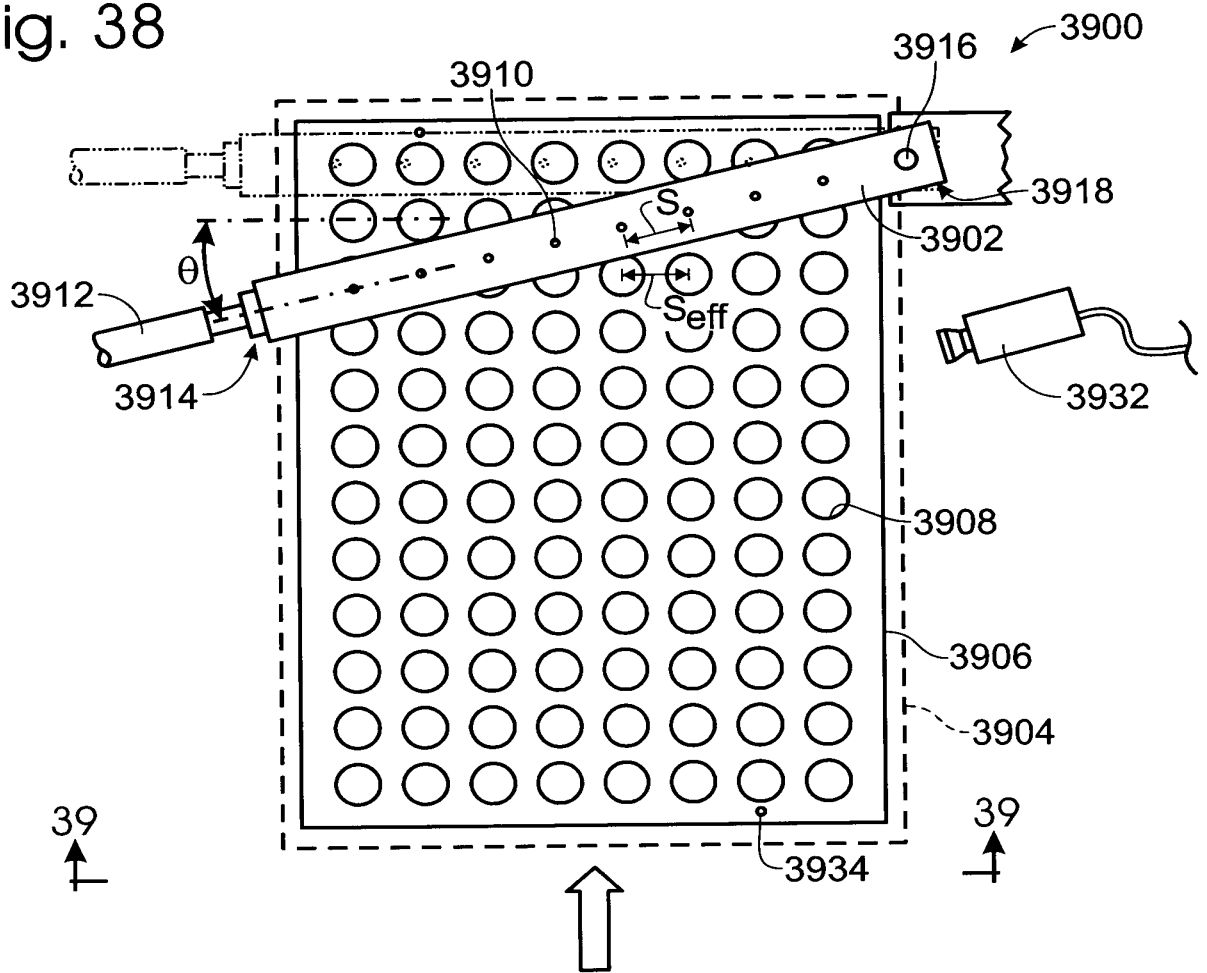


Fig. 39

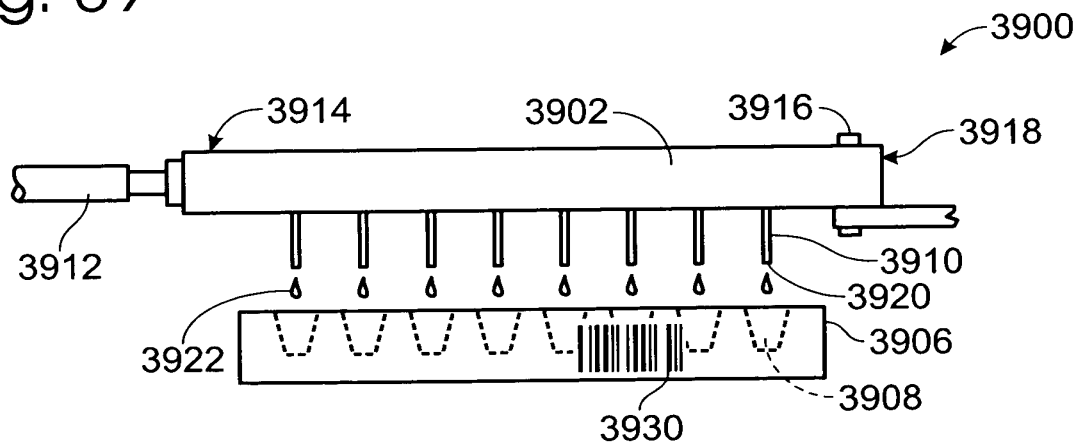


Fig. 40

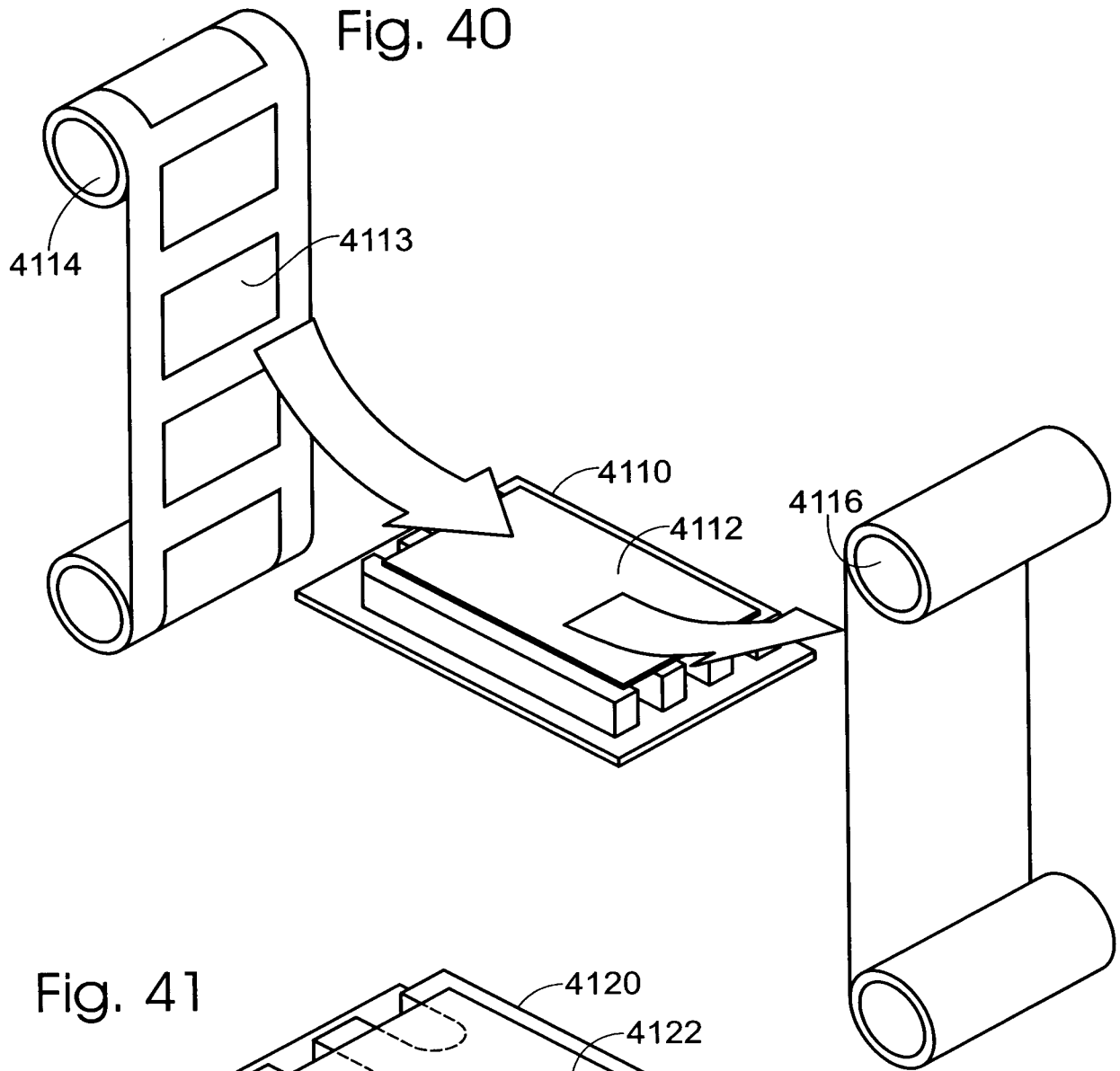


Fig. 41

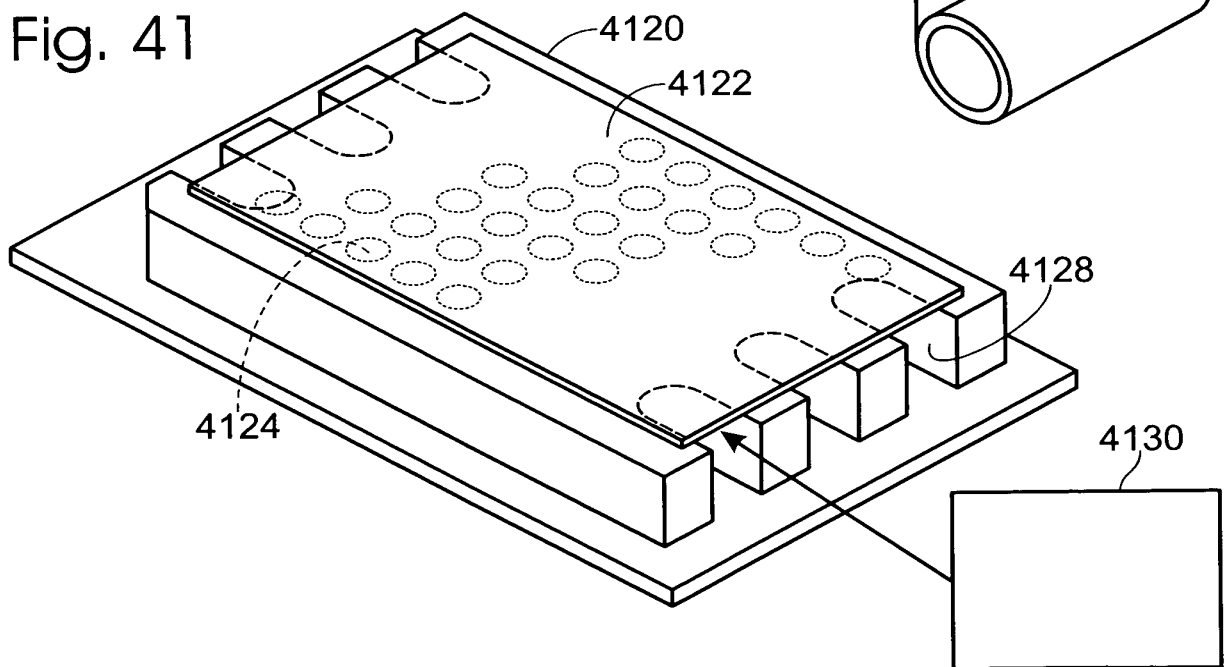


Fig. 42

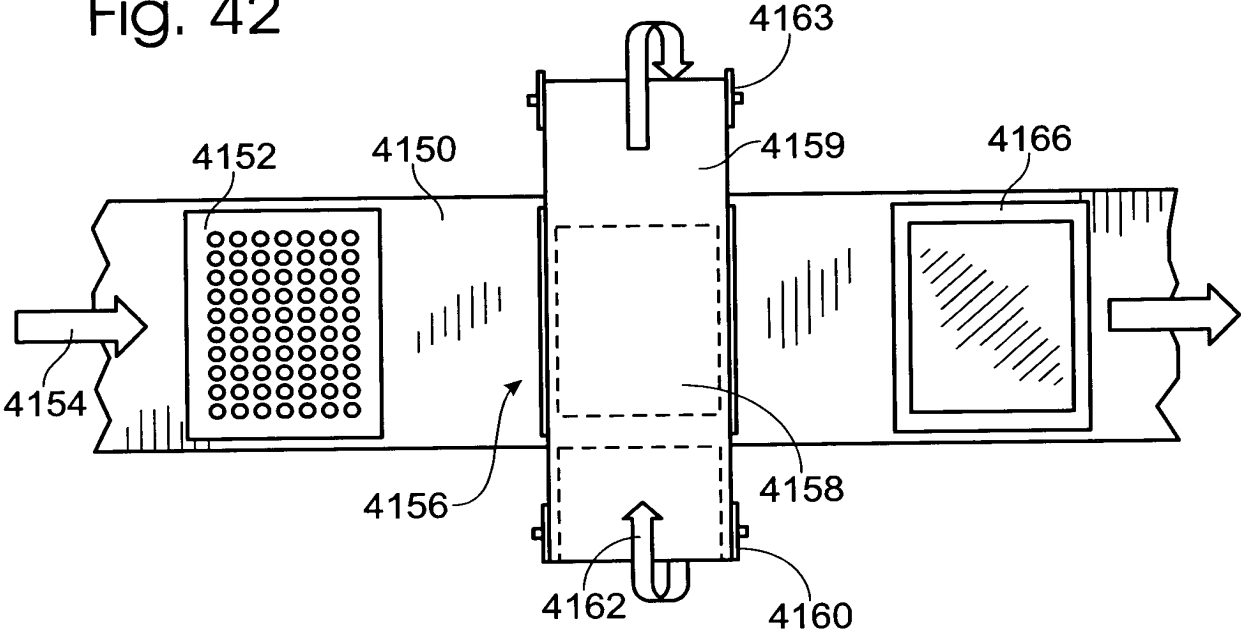


Fig. 43

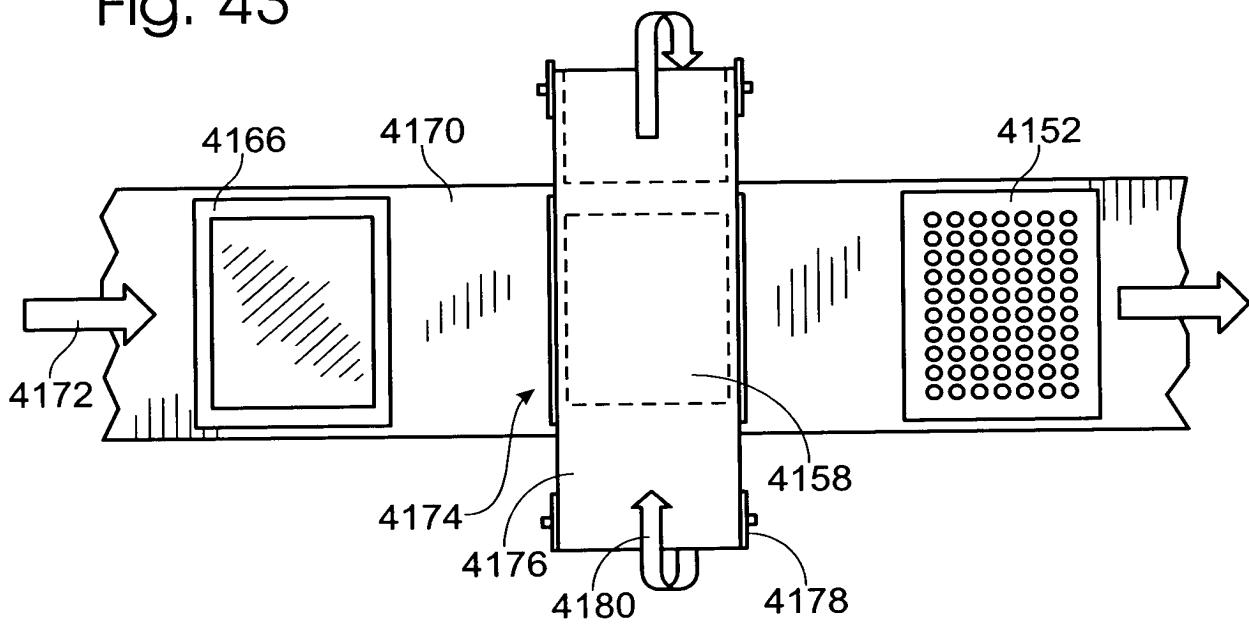


Fig. 44

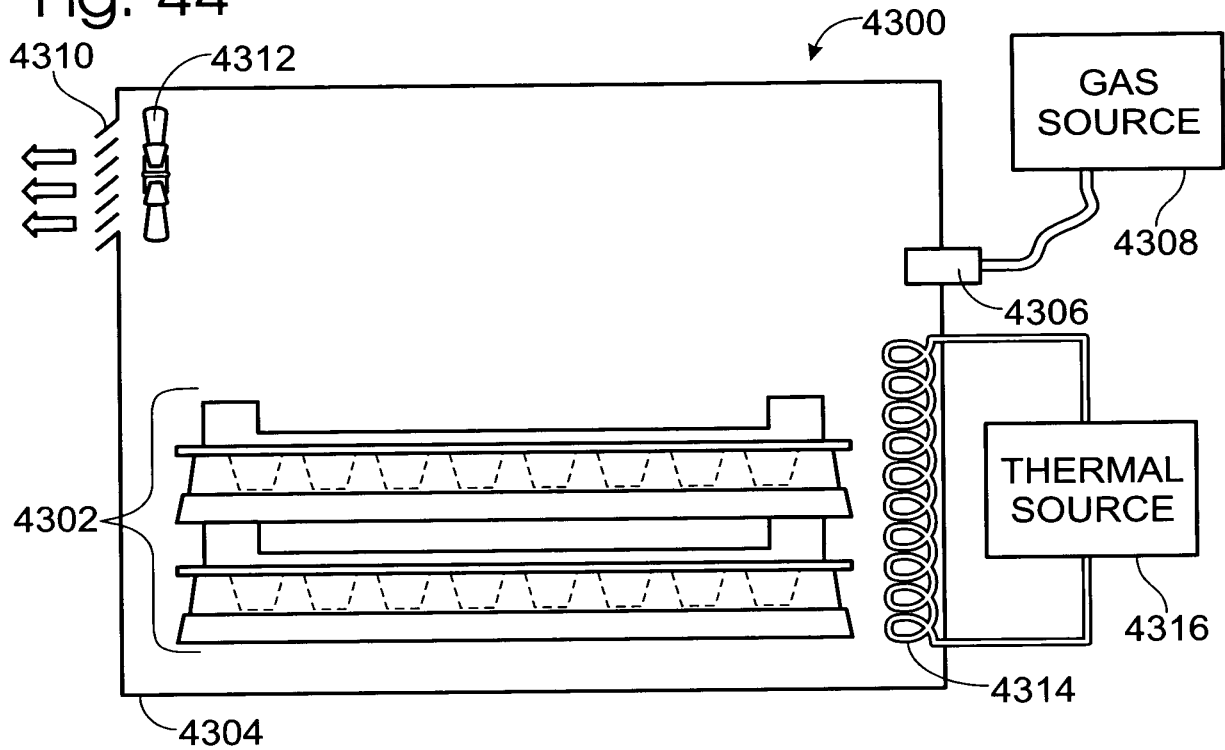


Fig. 45

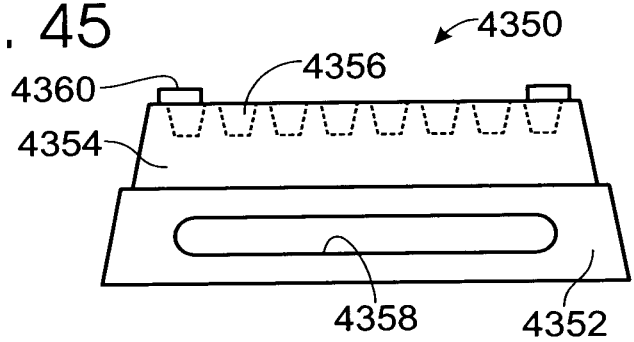


Fig. 46

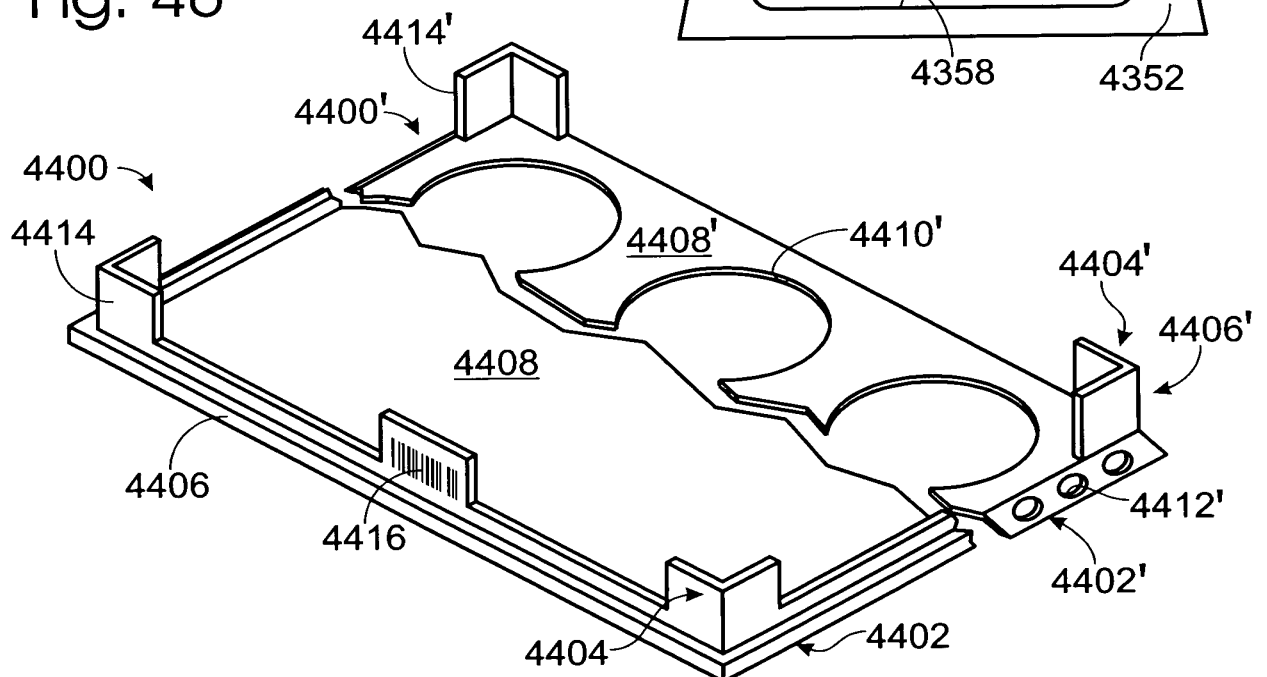




Fig. 47

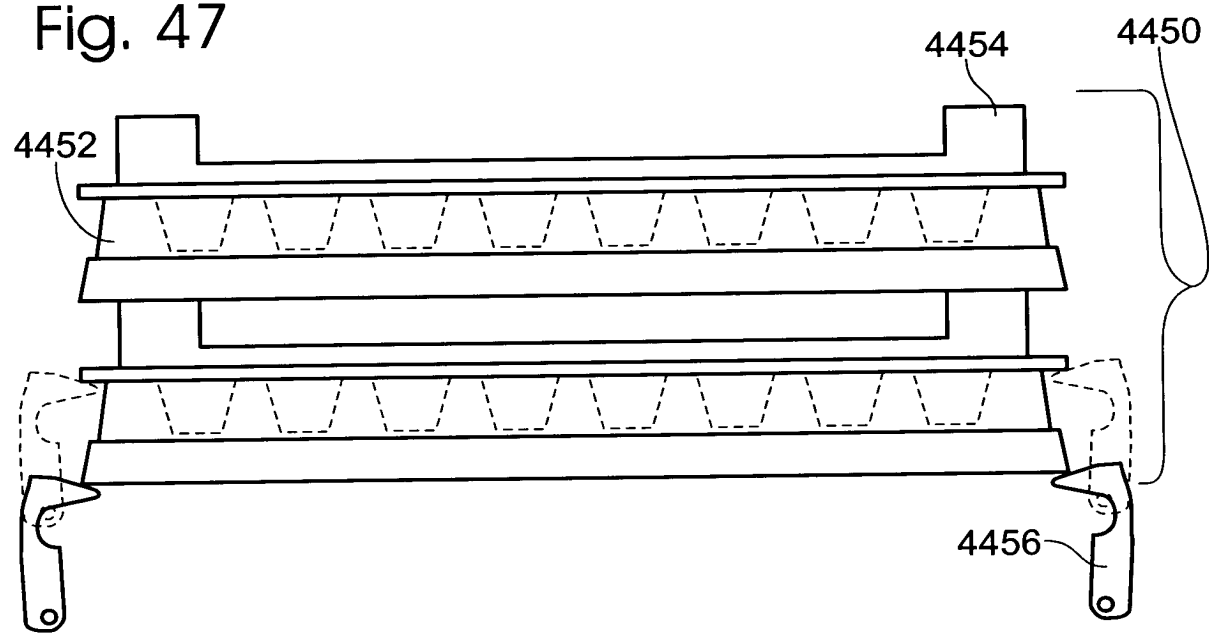


Fig. 48

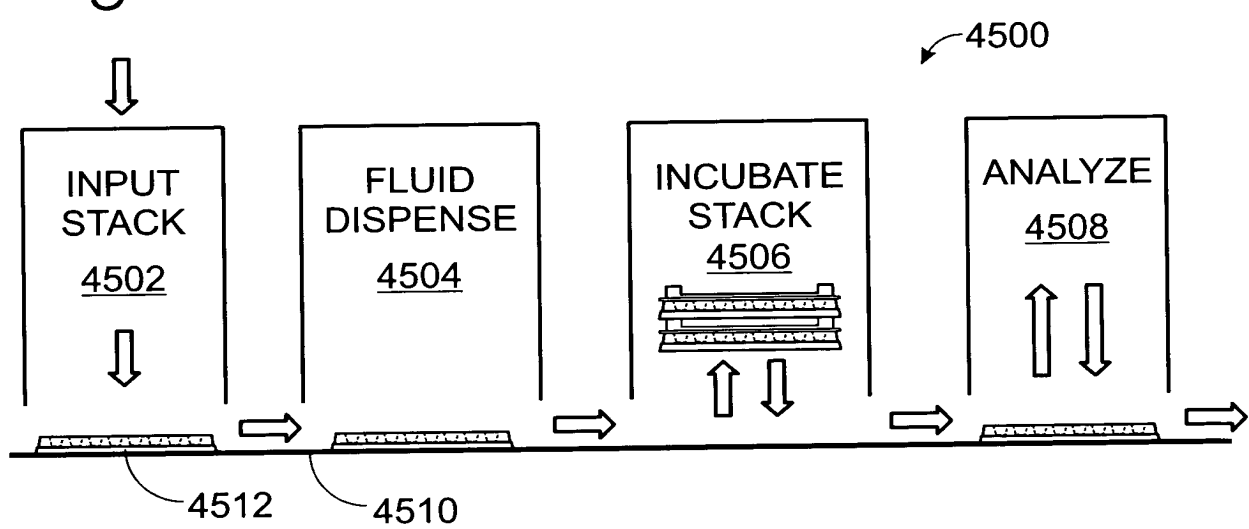
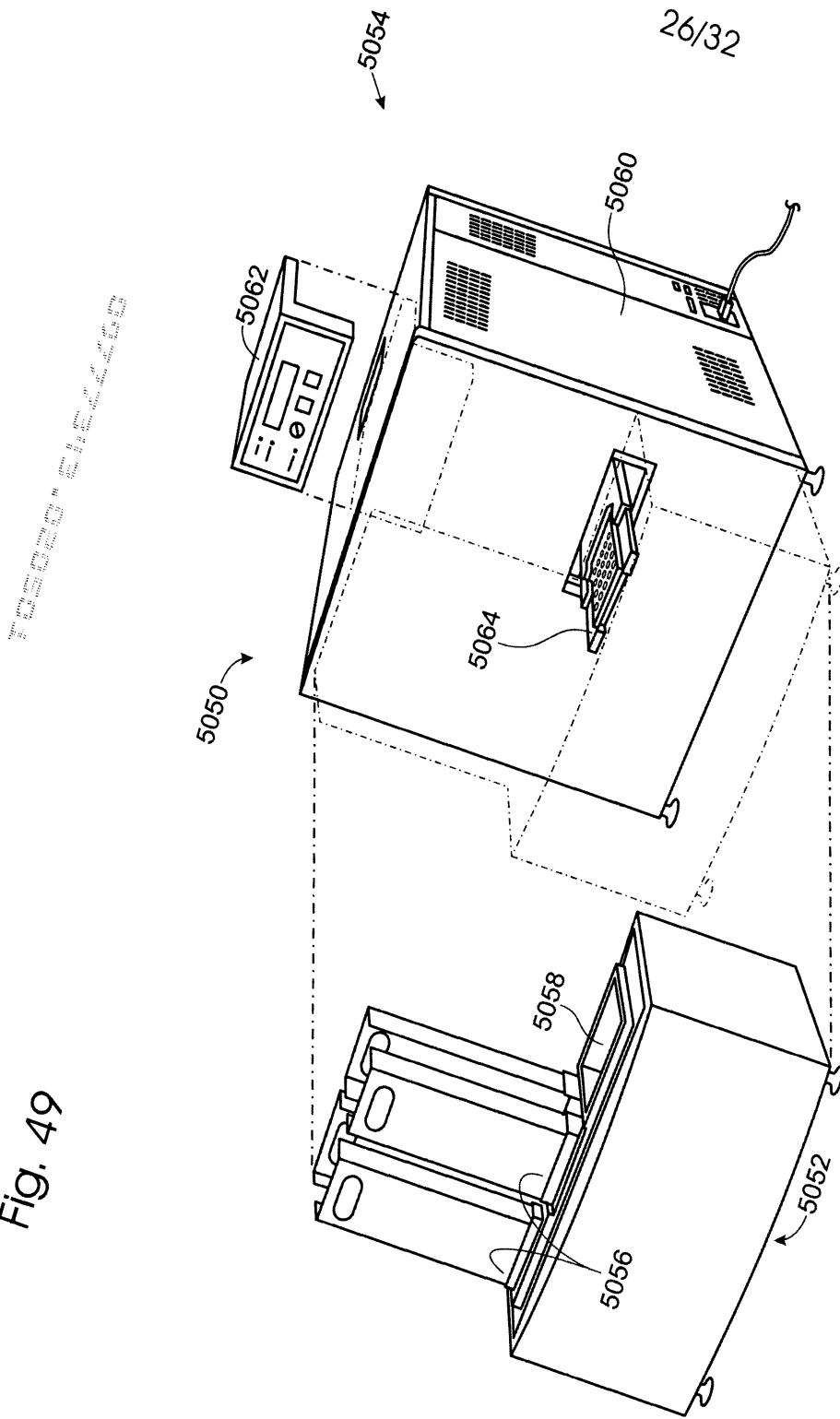


Fig. 49



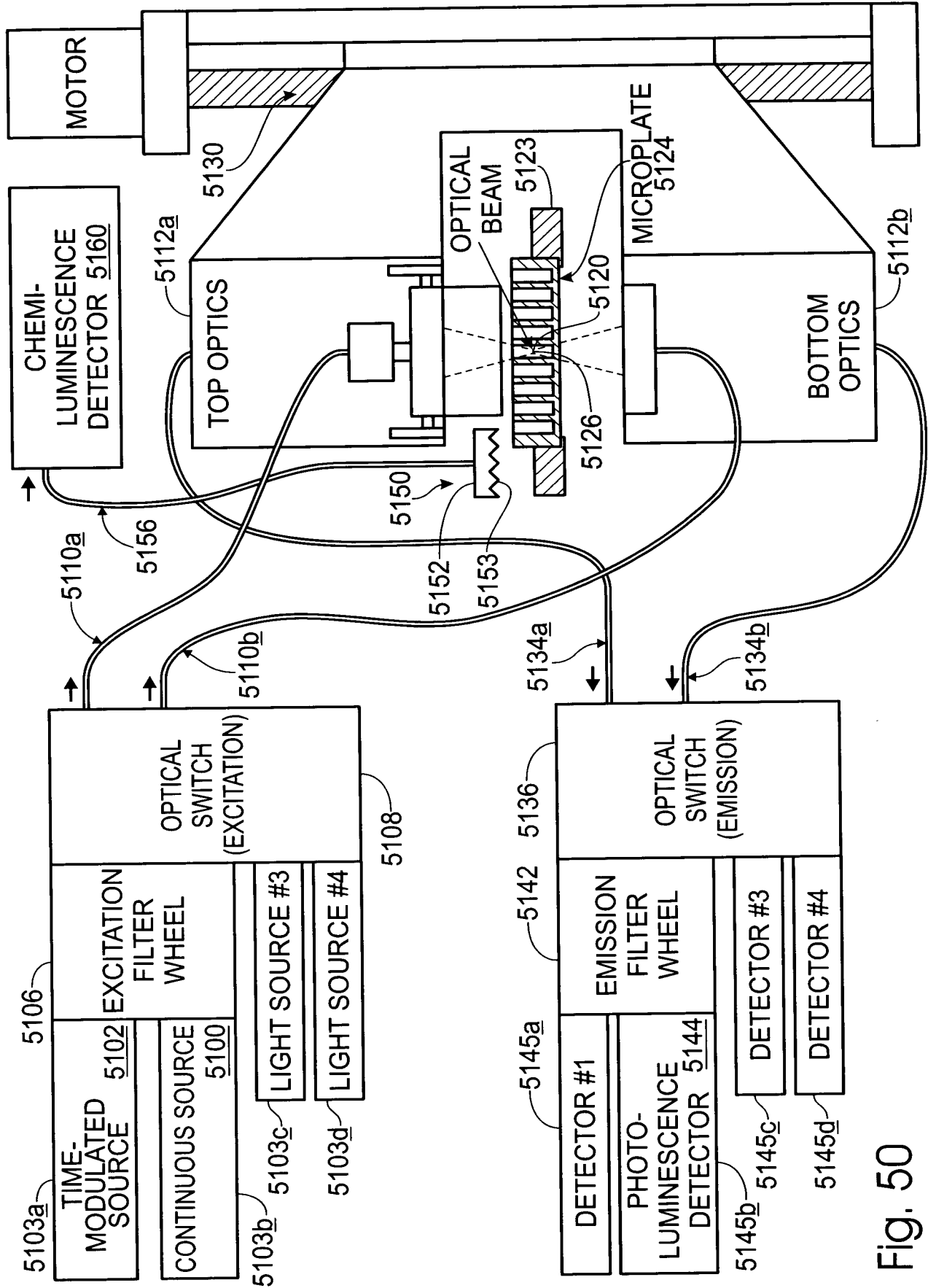


Fig. 50

Fig. 51

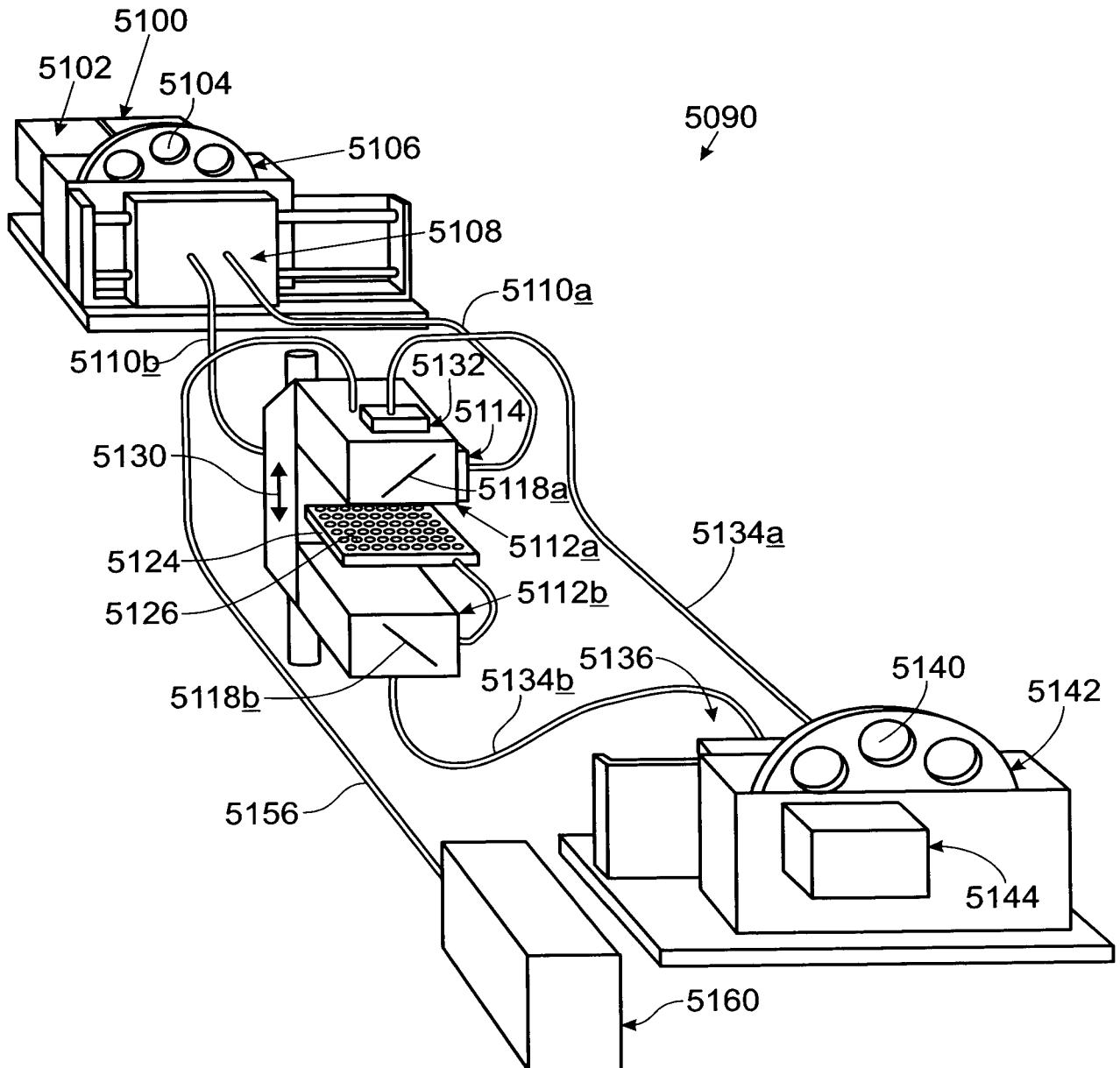


Fig. 52

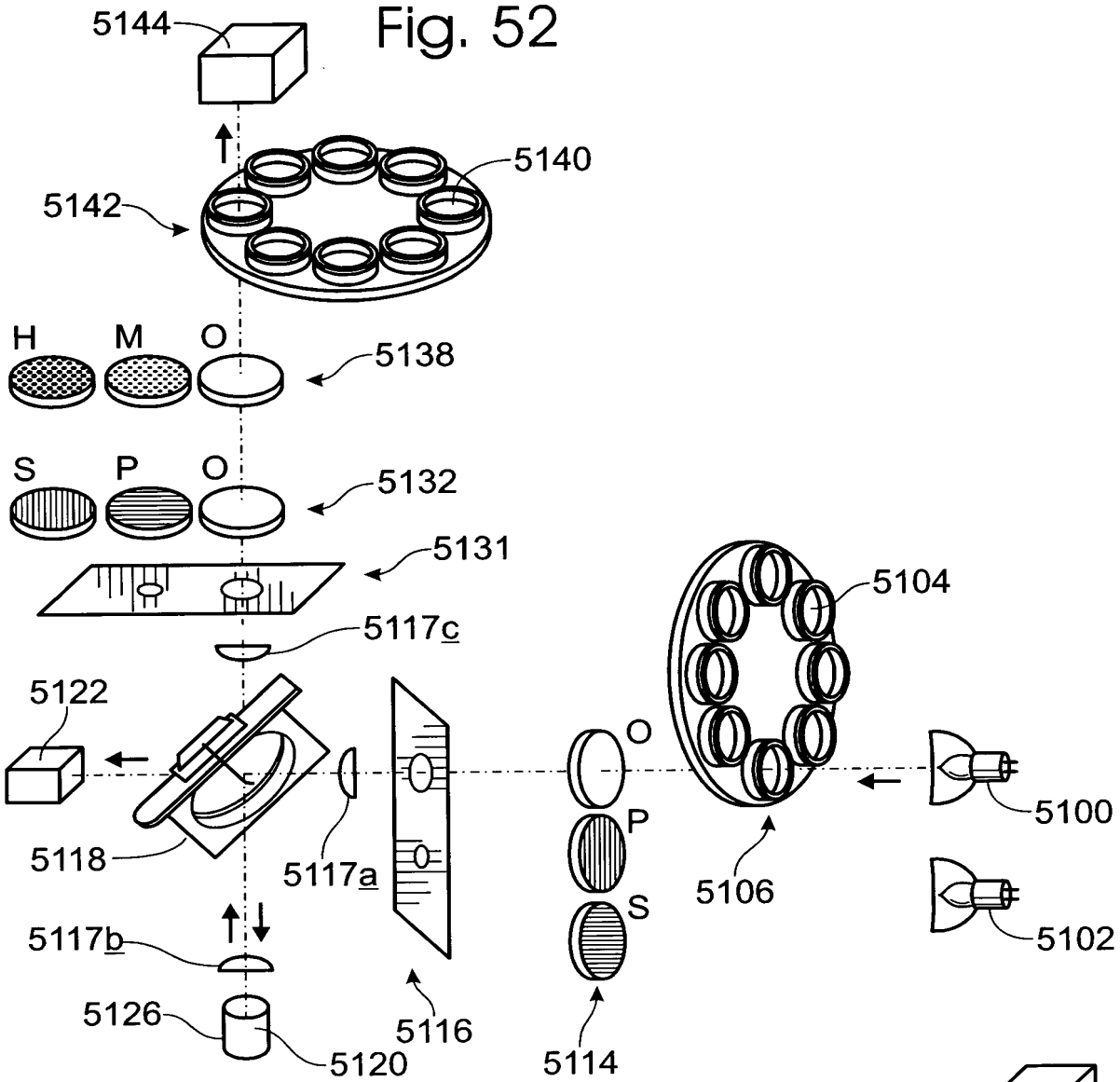


Fig. 53

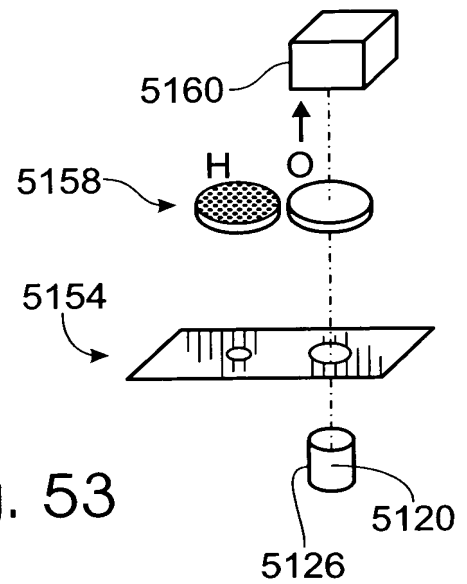


Fig. 54

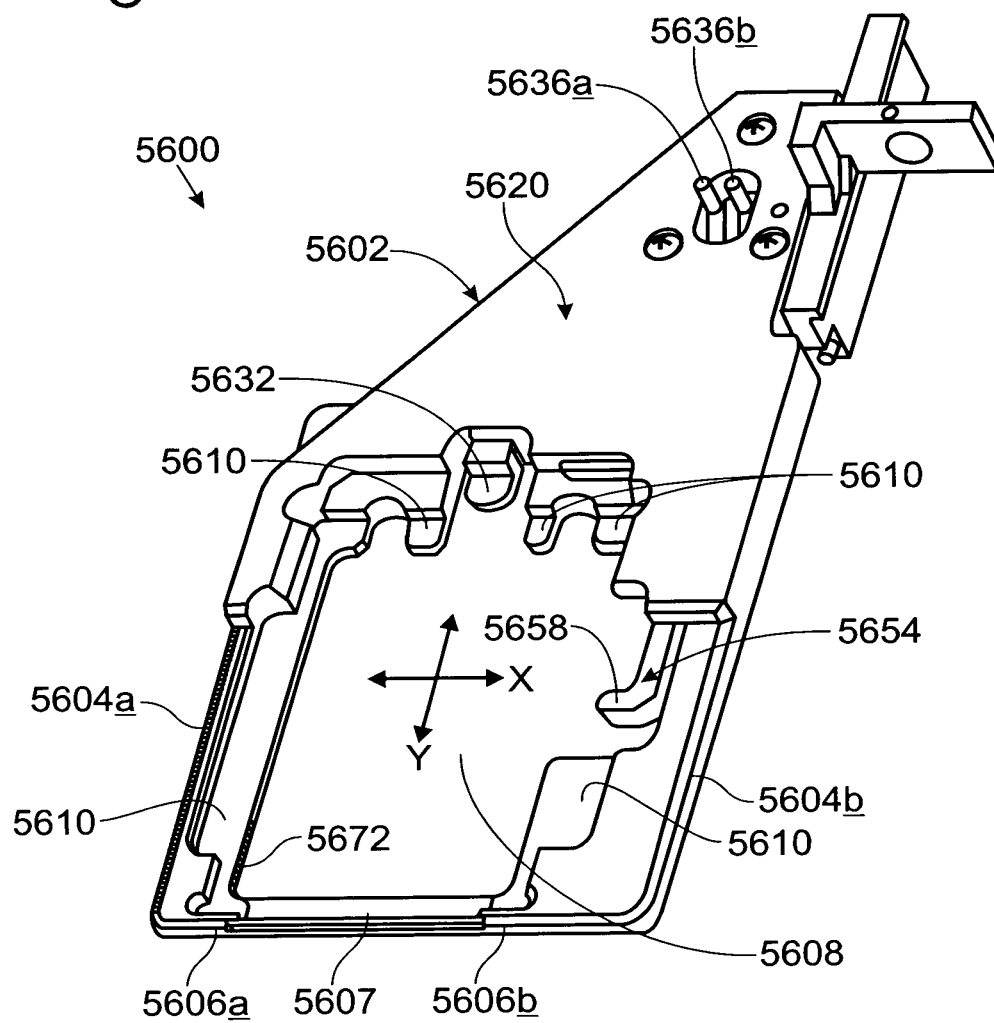


Fig. 55

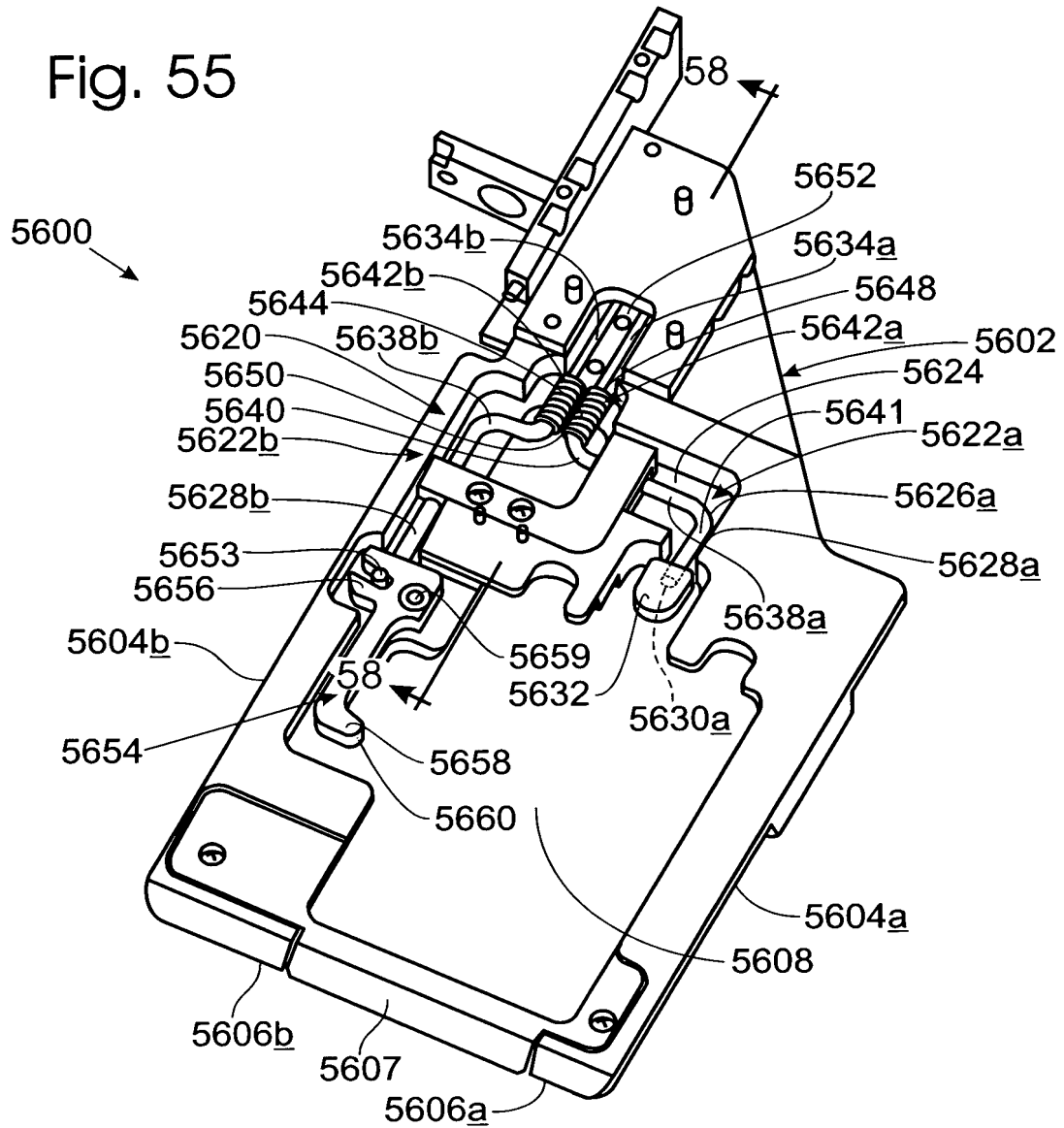


Fig. 56

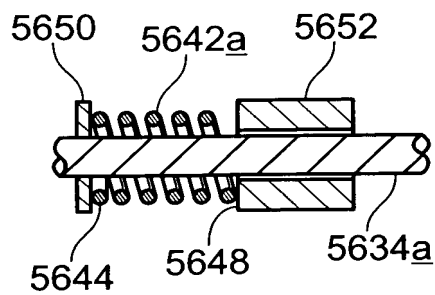


Fig. 57

